```
; 4/11/95
      ; with stop
      ;!!!!!!! note:for z8604 with external EEPROM & RS232 !!!!!!!!!!
                EQUATE STATEMENTS
                            .equ
                                        OfOH
                                                             ; expanded reg group F (WDT, SMR, PCON)
                          .equ
                                                          ; expanded reg group 0 (ports); B39 value for S1; B39 value for S2
      XRGRP0
                                    00Н
      S1B39
                            .equ 00000000b
                           .equ 00001000b
.equ 00000100b
.equ 00001000b
      S2B39
      S3B39
                                                             ; B39 value for S3
                                                             ; P32 S1 mask for Z86C04
; P33 S2 mask for Z86C04
; P31 S3 mask for Z86C04
      S1
      S2
                         .equ 00001000b
.equ 0000010b
.equ 0000010b
.equ 11111011b
.equ 0000010b
.equ 11111101b
.equ 0000001b
.equ 11111110b
.equ P2
     S3
     smr
                                                             ; stop mode recovery
                                                        ; stop mode recovery; P22 chip sel hi for 93c46; P22 chip sel lo for 93c46; P21 clk hi for 93c46; P21 clk lo for 93c46; P20 data out hi for 93c46; P20 data out lo for 93c46; chip sel port 93c46; data i/o port 93c46
     csh
    csl
    clockh
    clockl
    doh
121
     dol
     csport
famile
stands
                        .equ P2
     dioport
                                                             ;data i/o port 93c46
     clkport
                                                            ;clk port 93c46
    ; CONTROL REG AND INITIAL VALUES
Į.į.
     98
                       .equ 07FH
.equ 070H
.EQU 00H
.EQU 00H
.EQU 0000000B
.EQU 00001111B
.EQU 00000100B
.EQU 10010000B
.EQU 0000001B
.EQU 00001011B
.EQU 2500
lank
    STACKTOP
                                                             ; start of the stack
    STACKEND
                                                             ; end of the stack
; init general purpose reg to 00H
    GPR_INIT
RP_INIT
ingle.
                                                             ; init register pointer to 00
    IMR INIT
                                                        ; init intr mask reg (di)
; init intr priority reg
; init port 0&1 mode reg
; init port2 mode
    IPR INIT
    P01M INIT
    P2M_INIT
P3M_INIT
                           .EQU 10010000B
.EQU 00000001B
.EQU 00001011B
.EQU 250D
                                                        ; init port2 mode
; init port3 mode
; init prescalar 1 reg
; init counter/timer 1 reg /200
; init timer mode reg
; start timer
init port0
     PRET INIT
     T1 INIT
     TMR_INIT
TMR_START
                          .EQU
                                    0000000B
                                     00001100B
                          .EQU
    PO INIT
P2 INIT
                                   00000000B
00000000B
00000000B
                           .EQU
                            .EQU
                                                            ; init port2 ; init port3
    P3 INIT
                           .EQU
    SMR INIT
                           .EQU
                                   11111010B
                                                            ; init SMR reg bit1 hi OTP Lo Emulato
    PCON INIT
                            .EQU
                                     11111110B
                                                             ; init Port control reg
           ************************
              PREDEFINED CONTROL REG
                         .equ 255
.equ 254
.equ 253
.equ 252
    ;SPL
                                                            ; stack pointer
    GPR
                                                            ; general purpose
; register pointer
; cpu flags
    ;RP
    ;FLAGS
```

```
; IMR
                 .equ
                         251
                                          ; interrupt mask reg
                         250
 ; IRQ
                 .equ
                                          ; interrupt request
 ; IPR
                 .equ
                         249
                                         ; interrupt priority
 ; P01M
                         248-
                                         ; port 0 mode
                 .equ
                       248
247
246
                                         ; port 3 mode
; port 2 mode
 ; P3M
                 .equ
 ;P2M
                 .equ
 ;PRE1
                 .equ
                        243
                                          ; prescaler for timer 1
 ;T1
                         242
                                          ; timer 1
                 .equ
 ; TMR
                 .equ
                         241
                                          ; timer mode
 ;P3
                 .equ
                         3
                                          ; port 3
 ;P2
                                          ; port 2
                 .equ
 ;*******NON-PREDEFINED CONTROL REGISTERS USED WITH REGISTER POINTER*****
WDTMR
                 .EQU
                        r15
                                         ; watch dog timer RP=F0
 SMR
                       r11
                 .EQU
                                         ; stop mode recovery RP=F0
 PCON
                 .EQU
                         r0
                                          ; port control RP=F0
         INTERRUPTS
TIMER_ON_IMR .equ 001000000b ; turn on int for timer 1
last -
   GENERAL PURPOSE REGISTERS
; GENERAL PURPOSE REGISTER GROUP 00H-09H (00h-01H reserved)
REGGRP00
              .equ 00H
                      REGGRP00
REGGRP00+1
REGGRP00+2
200
                 .equ
                                      ; reserved
; reserved
; P2
; P3
41.5
                 .equ
                 .equ
•
                      REGGRP00+3
                 .equ
                 .equ REGGRP00+4
*X3XTMP
                                        ; trinary add to itself #
*3XTMP1
                         REGGRP00+5
                 .equ
                 .equ REGGRP00+5
.equ REGGRP00+6
X3XTMP2
X3XTMP3
                       REGGRP00+7
                 .equ
TRCXX
                 .equ
                       REGGRP00+8
                                         ; trinary number pointer
 TCNTR
                 .equ
                         REGGRP00+9
                       REGGRP00+9
REGGRP00+10
REGGRP00+11
                                        ; trinary counter
 X3XABCD
                                        ; trinary number
; trinary number
                 .equ
 X3XABCD1
                 .equ
 X3XABCD2
                       REGGRP00+12
                 .equ
                                         ; trinary number
                       REGGRP00+13
REGGRP00+14
REGGRP00+15
                                         ; trinary number
; Loop counter
 X3XABCD3
                 .equ
LPCNTR
                 .equ
B39
                 .equ
                                         ; button 1,2,3
                 .equ
                         r0
                                          ; reserved
                 .equ
                         rl
                                          ; reserved
                         r2
                                          ; P2
                 .equ
                         r3
                 .equ
                                          ; P3
 x3xtmp
                         r4
                 .equ
                                          ; trinary add to itself #
 x3xtmp1
                 .equ
                         r5
                        r6
 x3xtmp2
                 .equ
 x3xtmp3
                 .equ
                        r7
                       r8
 trcxx
                                        ; trinary number ptr
                 .equ
                                        ; trinary counter
; trinary number
 tcntr
                 .equ
                         r9
                         rl0
 x3xabcd
                 .equ
```

```
x3xabcd1
                .equ
                      r11
                                       ; trinary number
  x3xabcd2
                .equ
                       r12
                                       ; trinary number
                       r13
  x3xabcd3
                 .equ
                                       ; trinary number
 lpcntr
                 .equ
                        r14
                                       ; Loop counter
                       r15
  b39
                                       ; button 1,2,3
                 .equ
  ; GENERAL PURPOSE REGISTER GROUP 10H-1FH
  ; *******************************
  REGGRP10
                        10H
                .equ
  RC10B
                .equ
                        REGGRP10
                                       ; Roll Code 1 LSB
 RC11B .
                                       ; Roll Code 1
                .equ
                       REGGRP10+1
  RC12B
                .equ
                        REGGRP10+2
                                      ; Roll Code 1
                                      ; Roll Code 1 MSB
; Roll Code 2 LSB
; Roll Code 2
 RC13B
                 .equ
                        REGGRP10+3
 RC20B
                       REGGRP10+4
                 .equ
 RC21B
                .equ
                        REGGRP10+5
 RC22B
                .equ
                        REGGRP10+6
                                       ; Roll Code 2
 RC23B
                 .equ
                        REGGRP10+7
                                       ; Roll Code 2 MSB
 RC30B
                        REGGRP10+8
                                       ; Roll Code 3 LSB
; Roll Code 3
                 .equ
RC31B
                .equ
                       REGGRP10+9
RC32B
RC33B
                        REGGRP10+10
                .equ
                                       ; Roll Code 3
                .equ
                        REGGRP10+11
                                      ; Roll Code 3 MSB
FRAMEPTR
                                      ; frame pointer
                 .equ
                        REGGRP10+12
CODEPTR
                .equ REGGRP10+13  ; code pointer
.equ REGGRP10+14  ; bit pointer
                      REGGRP10+13
[sed BITPTR
RCPTR
                                     ; Rolling Code Reg Pointer
                .equ REGGRP10+15
#rc10b
                 .equ
                        r0
                                       ; Roll Code 1 LSB
rc11b
                                       ; Roll Code 1
                .equ
                        r1
"rc12b
                .equ
                        r2
                                       ; Roll Code 1
rc13b
rc20b
rc21b
                .equ
                        r3
                                       ; Roll Code 1 MSB
                                       ; Roll Code 2 LSB
; Roll Code 2
                 .equ
                        r4
                .equ
                        r5
rc22b
                        r6
                                       ; Roll Code 2
_____rc23b
                .equ
                        r7
                .equ
                                       ; Roll Code 2 MSB
 rc30b
                .equ
                        r8
                                       ; Roll Code 3 LSB
rc31b
                        r9
                                       ; Roll Code 3 ; Roll Code 3
                 .equ
==rc32b
                        r10
                .equ
 rc33b
                                       ; Roll Code 3 MSB
                .equ
                       r11
                .equ
 frameptr
                        r12
                                       ; frame pointer
 codeptr
                .equ
                        r13
                                       ; code pointer
                      r14
 bitptr
                .equ
                                       ; bit pointer
                                       ; Rolling Code Reg Pointer
 rcptr
                 .equ
                      r15
 rs232do .equ
                      r5
                               ; for RS-232 only
 rs232di
                 .equ
                        r6
 rscommand
                        r7
                .equ
 rs232docount
                .equ
                        r8
 rs232dicount
                .equ
                        r9
 rs232odelay
                .equ
                        r10
 rs232idelay
                        rll
                .equ
 rs232ccount
                .equ
                        r12
 rs232page
                        r13
                 .equ
 rsccount
                        r14
                 .equ
 rsstart
                        r15
                 .equ
 RS232D0
                 .EQU
                        REGGRP10+5
 RS232DI
                 .EQU
                        REGGRP10+6
 RSCOMMAND
                .EQU
                        REGGRP10+7
 RS232DOCOUNT
                .EQU
                        REGGRP10+8
 RS232DICOUNT
                 .EOU
                        REGGRP10+9
```

```
RS2320DELAY
                .EQU
                         REGGRP10+10
 RS232IDELAY
                 .EQU
                         REGGRP10+11
 RS232CCOUNT
                 .EQU
                         REGGRP10+12
 RS232PAGE
                 .EQU
                         REGGRP10+13
 RSCCOUNT
                         REGGRP10+14
                 .EQU
 RSSTART
                 .EQU
                         REGGRP10+15
                      00000100B
 RS2320S
                 .EQU
                                        ;RS232 output bit set
 RS2320C
                 .EQU 11111011B
                                        ;RS232 output bit clear
                                        ;RS232 output port
 RS2320P
                 .EQU
                         ΡO
                                      RS232 input port
                       P2
 RS232IP
                 .EQU
                         00010000B
                                         ;RS232 input mask
 RS232IM
                 .EQU
 ; GENERAL PURPOSE REGISTER GROUP 20H-2FH
 .equ 20H
 REGGRP20
                                      Trinary Roll Code REG's LSB; Trinary Roll Code REG's; Trinary Roll Code REG's; Trinary Roll Code REG's; Trinary Roll Code REG's
 TRC0
                         REGGRP20
                .equ
 TRC1
                .equ
                         REGGRP20+1
TRC2
                 .equ
                         REGGRP20+2
                 .equ REGGRP20+3
TRC4
                .equ REGGRP20+4
                                        ;Trinary Roll Code REG's
                                        Trinary Roll Code REG's
Trinary Roll Code REG's
Trinary Roll Code REG's
                .equ REGGRP20+5
.equ REGGRP20+6
.equ REGGRP20+7
ITRC5
TRC6
TRC8
                .equ REGGRP20+8
                                        ;Trinary Roll Code REG's
JTRC9
                .equ REGGRP20+9
                                        ;Trinary Roll Code REG's
SYNC1
                                        ;sync pulse frame1
                .equ REGGRP20+10
.equ REGGRP20+11
 TRC10
                                         ;Trinary Roll Code REG's
TRC11
                .equ REGGRP20+12
                                         ;Trinary Roll Code REG's
                .equ REGGRP20+13
# TRC12
                                        ;Trinary Roll Code REG's
                .equ REGGRP20+14
.equ REGGRP20+15
                                         ;Trinary Roll Code REG's ;Trinary Roll Code REG's
TRC13
FTRC14
trc0
                         r0
                                         ;Trinary Roll Code REG's LSB
                .equ
*trc1
                                         :Trinary Roll Code REG's :Trinary Roll Code REG's
                 .equ
                         r1
trc2
trc3
                         r2
                 .equ
                                         ;Trinary Roll Code REG's
                 .equ
                         r3
                         r4
                                         ;Trinary Roll Code REG's
                 .equ
                                         ;Trinary Roll Code REG's ;Trinary Roll Code REG's
                 .equ
 trc5
                         r5
 trc6
                 .equ
                         r6
                         r7
                                         :Trinary Roll Code REG's
                 .equ
 trc8
                 .equ r8
                                         ;Trinary Roll Code REG's
 trc9
                 .equ
                         r9
                                         ;Trinary Roll Code REG's
 sync1
                 .equ
                         r10
                                         ; sync pulse framel
                         r11
 trc10
                                         ;Trinary Roll Code REG's
                 .equ
 trc11
                         rl2
                                         ;Trinary Roll Code REG's
                 .equ
                       r13
 trc12
                                         ;Trinary Roll Code REG's
                 .equ
                                         ;Trinary Roll Code REG's ;Trinary Roll Code REG's
 trc13
                 .equ
                         r14
 trc14
                         r15
                 .eau
 ; GENERAL PURPOSE REGISTER GROUP 30H-39H (3Ah-3FH reserved for stack)
 REGGRP30
                       30H
                                        ; Trinary Roll Code REG's
; Trinary Roll Code REG's
; Trinary Roll Code REG's
 TRC15
                 .equ
                         REGGRP30
 TRC16
                 .equ
                         REGGRP30+1
 TRC17
                        REGGRP30+2
                 .equ
                .equ REGGRP30+3
 TRC18
                                        ; Trinary Roll Code REG's MSB
                .equ REGGRP30+4 ; sync pulse frame0
.equ REGGRP30+5 ; sync pulse frame0
 TRC19
 SYNCO
                 .equ
                         REGGRP30+5
                                        ; sync pulse frame0
```

```
RCMIR0
                 .equ
                         REGGRP30+6
                                        ; RC mirrored less LSB
                                        ; RC mirrored less
  RCMIR1
                         REGGRP30+7
                 .equ
  RCMIR2
                         REGGRP30+8
                                        ; RC mirrored less
                 .equ
  RCMIR3
                         REGGRP30+9
                                        ; RC mirrored less MSB
                 .equ
  trc15
                 .equ
                         r0
                                        ; Trinary Roll Code REG's
  trc16
                                        ; Trinary Roll Code REG's
                 .equ
                         rl
                                        ; Trinary Roll Code REG's ; Trinary Roll Code REG's MSB
  trc17
                 .equ
                         r2
  trc18
                 .equ
                         r3
                 .equ
                                        ; sync pulse frame0
  trc19
                         r4
  sync0
                 ·.equ
                         r5
                                        ; spare
  rcmir0
                 .equ
                         r6
                                        ; RC mirrored less LSB
  rcmir1
                                        ; RC mirrored less
                 .equ
                         r7
                                        ; RC mirrored less
  rcmir2
                 .equ
                         r8
  rcmir3
                                        ; RC mirrored less MSB
                 .equ
                        r9
  ; GENERAL PURPOSE REGISTER GROUP 40H-4FH
  ; ********************************
REGGRP40
                        40H
                 .equ
XMTREG
                 .equ
                         REGGRP40
  LPCTR
                         REGGRP40+1
XR00
                 .equ
                        REGGRP40+2
                 .equ
XMTREG1
                 .equ
                        REGGRP40+3
ACODEPTR
                         REGGRP40+4
                 .equ
MTFLAG
                 .equ
                         REGGRP40+5
DIVBY10 TRCPTR
                        REGGRP40+6
                 .equ
                 .equ
                        REGGRP40+7
L TEMPH
                        REGGRP40+8
                 .equ
                                        ;ee
, TEMPL
                 .equ
                         REGGRP40+9
                                        ;ee
TEMP
                        REGGRP40+10
                 .equ
                                        ;ee
MILMEN
MTEMPL
  MTEMPH
                 .equ
                         REGGRP40+11
                                        ; memory tem eeprom
                        REGGRP40+12
                 .equ
                                       ; memory tem eeprom
                                       ;memory tem eerom
;serial data to/from eeprom
*** MTEMP
                         REGGRP40+13
                 .equ
SERIAL
                        REGGRP40+14
                 .equ
ADDRESS
                 .equ
                      REGGRP40+15
                                        ;eeprom address
xmtreg
                         r0
                 .equ
  lpctr
                         rl
                 .equ
  xr00
                         r2
                 .equ
  xmtreg1
                         r3
                 .equ
  acodeptr
                         r4
                 .equ
  mtflag
                         r5
                 .equ
  divby10
                         r6
                 .equ
                         r7
  trcptr
                 .equ
  temph
                 .equ
                         r8
  templ
                 .equ
                         r9
  temp
                         r10
                 .equ
  mtemph
                 .equ
                         r11
  mtempl
                 .equ
                         r12
  mtemp
                  .equ
                         r13
  serial
                         r14
                 .equ
  address
                 .equ
                         r15
  ; GENERAL PURPOSE REGISTER GROUP 50H-5FH
  REGGRP50
              .equ
                         50H
  ACODE 0BM -
                         REGGRP50
                 .equ
  ACODE1BM
                 .equ
                         REGGRP50+1
```

```
ACODE2BM
                   .equ
                          REGGRP50+2
   ACODE3BM
                   .equ
                          REGGRP50+3
   ACODE 4BM
                   .equ
                          REGGRP50+4
   ACODE5BM
                          REGGRP50+5
                   .equ
   ACODE 6BM
                   .equ
                          REGGRP50+6
   ACODE7BM
                   .equ
                          REGGRP50+7
   ACODE8BM
                   .equ
                          REGGRP50+8
   ACODE 9BM
                   .equ
                          REGGRP50+9
   ACODE10BM
                   .equ
                          REGGRP50+10
   ACODE11BM
                   .equ
                          REGGRP50+11
   ACODE12BM
                   .equ
                          REGGRP50+12
   ACODE13BM
                          REGGRP50+13
                   .equ
   ACODE14BM
                   .equ
                          REGGRP50+14
   ACODE15BM
                          REGGRP50+15
                   .equ
   acode0bm
                   .equ
                          r0
   acode1bm
                   .equ
                          r1
   acode2bm
                   .equ
                          r2
  acode3bm
                   .equ
                          r3
acode4bm
                  .equ
                          r4
  acode5bm
                  .equ
                          r5
  acode6bm
                   .equ
                          r6
  acode7bm
                  .equ
                          r7
lask
  acode8bm
                  .equ
                          ŗ8
acode9bm
                  .equ
                          r9
  acode10bm
                  .equ
                          r10
acode11bm
                  .equ
                          r11
  acode12bm
                  .equ
                          r12
   acode13bm
                  .equ
                          r13
  acode14bm
                  .equ
                          r14
  acode15bm
                  .equ
                          r15
23 lz
   ; GENERAL PURPOSE REGISTER GROUP 60H-6FH
  REGGRP 60
                  .equ
                          60H
  ACODE16BM
                  .equ
                          REGGRP 60
  ACODE17BM
                  .equ
                          REGGRP60+1
  ACODE18BM
                  .equ
                          REGGRP 60+2
  ACODE 19BM
                          REGGRP 60+3
                  .equ
  RSFLAG
                  .equ
                          REGGRP 60+4
  XMTFLAG
                  .equ
                          REGGRP 60+5
  AC19
                  .equ
                          REGGRP60+6
  RCP
                  .equ
                          REGGRP 60+7
  LPCNTRA
                  .equ
                          REGGRP60+8
  FRMCTRH
                  .equ
                          REGGRP60+9
  FRMCTRL
                  .equ
                          REGGRP60+10
  ATMP
                  .equ
                          REGGRP60+11
                                         ;acode tmp storage
  ;acode h
                  .equ
                          REGGRP60+12
                                         ;acode rom pointerh
  ;acode l
                                         ;acode rom pointerl
                  .equ
                          REGGRP 60+13
  LPCTR1
                  .equ
                         REGGRP60+14
                                         ;counter
  APTR
                  .equ
                         REGGRP60+15
                                         ;acode ram pointer
  acode16bm
                  .equ
                         r0
  acode17bm
                  .equ
                         rl
  acode18bm
                         r2
                  .equ
  acode19bm
                  .equ
                         r3
  rsflag
                  .equ
                         r4
  xmtflag -
                  .equ
                         r5
  ac19
                  .equ
                         r6
```

```
r7
  rcp
                  .equ
  lpcntra
                          r8
                  .equ
                          r9
  frmctrh
                  .equ
                          r10
  frmctrl
                  .equ
  atmp
                  .equ
                          r11
                                          ;acode tmp storage
                          rr12
  acode
                  .equ
                                          ;acode register pair
  acode_h
                          r12
                  .equ
                                          ;acode rom pointer h
                  .equ
                                          ;acode rom pointer 1
  acode_1
                          r13
  lpctrI
                  .equ
                          r14
                                          ;counter
  aptr
                          r15
                                          ;acode ram pointer
                  .equ
   ; MACROS
  WDT
                  .macro
                  .byte
                          5fh
  WDH
                  .macro
                  .byte
                          4fh
                  .endm
  FILL
                  .macro
1,5%
                  .byte
                          OFFh
iji.
į datė
Interrupt Vector Table
[sec
0000Н
                  .org
4,50
                         000CH
                                          ;IRQ0 P3.2
122
                  .word
                          000CH
                                          ;IRQ1, P3.3
                  .word
                   .word
                          000CH
                                          ; IRQ2, P3.1
                                          ;IRQ3, S/W generated ;IRQ4, S/W generated
                          000CH
                  .word
                          000CH
                  .word
                                          ; IRQ5, Timer T1
                  .word
                          T1_INT
                  START (poweron reset or stop mode)
  .page
                           000CH
                  .org
  start:
  START:
                  di
                                          ; disable interrupts for init
                  WDT
                                          ; hit WDT
                  Internal RAM Test and Reset All RAM = ?? mS
                  srp
                          #XRGRPF
                                         ;no,point to control group use stack
```

```
ld
                     r15,#4
                                   ;r15= pointer (bottom of RAM)
 write again:
               clr
                      @r15
                                    ;write RAM(r5)=0 to memory
               inc
                      r15
               ср
                      r15, #7FH
                                    ;top of ram 7F
               jr
                      ult, write again
    *********************
        initialize registers
      ****************
                      #REGGRP00
               srp
                                    ; set the group
               ld
                      SMR,#SMR_INIT ; set smr reg
 ; STACK INITIALIZATION
SETSTACK:
                      spl, #STACKTOP
                                           ; set the start of the stack
1.[3
last.
  TIMER INITIALIZATION
1 d
                      prel, #PRE1 INIT
                                          ; set the prescaler
               ld
                      t1, #T1 INIT
                                          ; set the counter
l,ı,i
                      tmr, #TMR START
               lđ
                                           ; turn on the timer
************************
PORT INITIALIZATION
  **********************
am if
               clr
                    ΡO
                                    ; set port0 lo
; set port2 lo
4,4
               clr
                      P2
               clr
                      P3
                                    ; set port3 lo
                      p3m, #P3M_INIT
p2m, #P2M_INIT
               ld
                                          ; set port 3 mode
; set port 2 mode
               ld
                     p01m, #P01M_INIT
               ld
                                           ; set port 1 mode
 ; INTERRUPT INITIALIZATION
 SETINTERRUPTS:
               ld
                      ipr, #IPR INIT ; set the priority for timer
       initialize EEPROM by reading it
               CALL
                      READMEMORY
                                    ; settle EE lines
 ; MAIN LOOP
           CKBUTTON1
 CKBUTTON1:
               CALL
                     CKB1
               LD
                      ACODE19BM, AC19
               LD
                      RCPTR, RCP
```

```
Get Rolling Code From EEPROM
 EE_ADDRESS 11->RC10B, RC11B, RC12B, RC13B
       EE ADDRESS 13->RC20B, RC21B, RC22B, RC23B
      EE ADDRESS 15->RC30B, RC31B, RC32B, RC33B
 INITPTRS:
            srp
                   #REGGRP00
             add
                   RCPTR, #3
                              ; TOP OF RC RAM
             CP
                   RCPTR, #RC13B
             JR
                  nz, CKRC23
                  ADDRESS, #11
             LD
                              ;EE PTR
             JR
                  GETRCODE
 CKRC23:
             CP
                  RCPTR, #RC23B
             JR
                  nz, APTR15
                  ADDRESS, #13
             LD
            JR
                  GETRCODE
APTR15:
                  ADDRESS, #15
            LD
GETRCODE:
GETRCODE1:
            LD
                  lpcntr,#2
            CALL
                  READMEMORY
LD
                  @RCPTR, MTEMPH ; HI BYTE
واجدأ
            DEC
                  RCPTR
            LD
                  @RCPTR, MTEMPL
                             ;LO BYTE
            DEC
                  RCPTR
DEC
                  ADDRESS
i.i.i
            DJNZ
                  lpcntr, GETRCODE1
                                   :done?
             INC
                  RCPTR
::::::
      *******************
    osk; *
INCRCBY3:
            srp
                  #REGGRP10
            ADD
                  @rcptr, #3d
                                   ;Add 3 to Rolling Code
es la
            LD
                  bitptr, #3d
 INCRNEXT:
            INC
                  rcptr
            ADC
                  @rcptr,#0
            DJNZ
                  bitptr, INCRNEXT
 ***************
     Store updated Rolling Code in EEPROM
 ************
            CALL
                  CKB1
                              ; SAME BUTTON STILL
                  ACODE19BM, AC19 ; PRESSED?
            CP
            qį
                  nz, SCHTOPP
                  #REGGRP60
            srp
            ADD
                  ADDRESS, #2
                              ;START EEPROM ADDRESS
SAVRCODE:
            LD
                  lpcntra, #2
SAVRCODE1:
            LD
                  MTEMPH, @RCPTR ; hi byte
            DEC
                  RCPTR
            LD
                  MTEMPL, @RCPTR
                             ;lo byte
            CALL
                  WRITEMEMORY
            DEC
                  RCPTR
            DEC
                  ADDRESS
            DJNZ
                  lpcntra, SAVRCODE1
            INC
                  RCPTR
```

```
get ACODE0BM-ACODE18BM from eeprom
                  srp
                          #REGGRP40
                  ld
                          address, #9
                                                    ;highest eeprom addr
                  ld
                          acodeptr, #ACODE18BM
                                                   ; highest acode ram addr
 GETACODE:
                  CALL
                          READMEMORY
                  ld
                                                    ;hi byte
                          @acodeptr, mtemph
                  DEC
                          acodeptr
                  CP
                          acodeptr, #4Fh
                                                   ;4fh? done?
                  JR
                          z, ACODONE
                  ld
                          @acodeptr,mtempl
                  DEC
                          address
                  djnz
                          acodeptr, GETACODE
 ACODONE:
       Mirror RCX0,1,2,3 into RCMIR0,1,2,3 and zero MSB
MIRROR:
                          #REGGRP10
                  srp
                  ld
                          codeptr,#RCMIR3
                                                   ;RCMIR3 FIRST
NBYTE:
                  ld
                          bitptr, #08d
                                                   ; set bit counter to 7
SHIFT:
                  RL
                                                   ; shift RC into carry
                          @rcptr
RRC
                                                    ; shift carry into mirror
                          @codeptr
                  DJNZ
                          bitptr,SHIFT
CP
                          codeptr, #RCMIR3
                                                   ; if RCMIR3 then
12
                  JR
                          nz, NOTRC3
255
                                                   ; set bit 7 RCMIR3 to 0
                  AND
                          RCMIR3, #011111111b
NOTRC3:
                  DEC
                          codeptr
                                                   ;next rcmir
                  INC
                          rcptr
|ask
                  СP
                          codeptr, #35H
4
                  JR
                          nz, NBYTE
)
132 }
                  sub
                          rcptr, #4
         Trinary conversion & store in TRC0-TRC19
                          #REGGRP00
                  srp
                                           ;set reg pntr
                  LD
                                           ; ZERO OUT TRC PREVIOUS TRINARY #'s
                          lpcntr,#36H
 ZAGN:
                  DEC
                          1pcntr
                  CLR
                          @lpcntr
                  CP
                          lpcntr, #20H
                  JR
                          nz, ZAGN
                  LD
                          TRCXX, #TRC19
                  LD
                          RCPTR, #20
 CALCTRNY:
                  CP
                          RCPTR, #01
                                           ; calc trinary number
                  JR
                          z,X3XX1
                  CALL
                          ENTR3
                  CP
                          RCPTR, #02
                                           ;=2?
                          z, TRICONVXX
                  JR
                  SUB
                          RCPTR, #2
                          tcntr, RCPTR
                  LD
                  ADD
                          RCPTR, #2
```

```
The state of the s
```

```
CALL
                           ENTR3A
ADDAGN:
                  CALL
                           AD3XX
                                            ;add to itself
                  CALL
                           AD3XX
                  CALL
                           XFER
                  DJNZ
                           tcntr, ADDAGN
                                            ; TCNTR=0?
                  JR
                           TRICONVXX
X3XX1:
                  LD
                           x3xabcd, #01h
                  clr
                           x3xabcd1
                  clr
                           x3xabcd2
                  clr
                           x3xabcd3
TRICONVXX:
                  SBC
                           RCMIR0, x3xabcd
                  SBC
                           RCMIR1, x3xabcd1
                  SBC
                           RCMIR2, x3xabcd2
                  SBC
                           RCMIR3, x3xabcd3
                  JR
                           C, ADDXXBK
INCTRCXX:
                  INC
                           @TRCXX
                  JR
                           TRICONVXX
ADDXXBK:
                  CCF
                  LD
                           lpcntr, x3xabcd
                  ADC
                           RCMIRO, lpcntr
                  LD
                          lpcntr,x3xabcd1
RCMIR1,lpcntr
                  ADC
                  LD
                          lpcntr,x3xabcd2
                  ADC
                          RCMIR2, lpcntr
                 LD
                          lpcntr, x3xabcd3
                          RCMIR3, lpcntr
                 ADC
                 DEC
                          RCPTR
                                            ; next lower power of 3
; done with TRC00-TRC19 ?
                 DEC
                          TRCXX
                  CP
                          TRCXX, #SYNC1
                                            ; sync bit position?
                  JR
                          nz, NXCP
                 DEC
                          TRCXX
                                            ; yes
NXCP:
                 CP
                          TRCXX, #1FH
                                            ;no
                  JR -
                          nz, CALCTRNY
        Transmit initialization
 *******
       initialize RSFLAG *
                 tm
                          RS232IP, #RS232IM
                                                    ;DATA IN LO?
                 JR
                          z, disrscall
                 ld
                          RSFLAG, #0FFh
                                                    ;set rs232 call enable flag
disrscall:
                 srp
                          #REGGRP40
                                                    ;set reg pntr
                 LD
                          SYNC1, #02H
                                                    ; INITIALIZE SYNC1
                 LD
                          acodeptr, #ACODE0BM-1
                                                    ;initialize
                 LD
                          trcptr, #SYNC0
                                                    ; for xmt
                 LD
                          BITPTR, #OffH
                 LD
                          CODEPTR, #SYNCO
                 LD
                          xmtreg, SYNCO
                 LD
                          FRMCTRH, #02H
                                                    ;04H INIT FRAME COUNTER H
                 LD
                          FRMCTRL, #0A0H
                                                    ; OBH INIT FRAME COUNTER L
```

```
clr
                          address
                                                  ;address for RS232 xfer
                          RS232DOCOUNT, #11D
                  LD
                                                  ;turn off RS232 output
                  LD
                          RS232DICOUNT, #0FFH
                                                  turn off RS232 input
                                                  ;incoming data present
                  LD
                          RSCOMMAND, #OFFH
                                                  ;turn off rs232 command
                  clr
                          mtflag
                                                  ; initialize mtflag
         *************************
          Wait for transmit INT
          ************************
                          IMR, #TIMER_ON_IMR
                                                 ; INT Mask enable
  LOOP:
                  ΕI
                                                 ; enable INT
  RSDATRDY:
                  CP
                         RSCOMMAND, #OFFH ; RS232 DATA IN ?
                  JR
                          Z,XMTMTL
                  CP
                         mtflag,#0
The species of the species of
                  jr
LD
                          z, RCVMTH
  RCVMTL:
                         mtempl, RS232DI ; input mtempl
                  ld
                         RSCOMMAND, #0FFH
                  clr
                         mtflag
                                         ;reset mtflaq
322
                  call
                          WRITEMEMORY
                                         ;write mtempl to EEprom
;read mtempl from EEprom
                 call
                         READMEMORY
  XMTMTH:
                 ld
                         RS232DO, mtemph ;rs232 echo back
                 ld
                         RSSTART, #OFFH
                                         ;mtemph
1,4,5
                         RS232DOCOUNT
                 clr
                 ld
                         XMTFLAG, #OFFh
12
                                         ;set flag
                 inc
                         address
şaa la
                 СР
                         address, #16D
                 jr
                         nz, XMTMTL
lagi:
                 clr
                         address
                                         ;set address to 0
                  ٦r
                         XMTMTL
  RCVMTH:
                         mtemph, RS232DI ; mtemph
                 ld
                 ld
                         RSCOMMAND, #0FFH
                 ld
                         mtflag, #OFFH
 XMTMTL:
                 СР
                         XMTFLAG, #0FFh ; ck for xmt first byte
                 jr
                         nz, CKSWS
                 ср
                         RS232DOCOUNT, #11D; test for output done
                  jr
                         nz, CKSWS
                 Īđ
                         RS232DO, mtempl ; echo back mtempl
                         RSSTART, #OFFH
                 ld
                 clr
                         XMTFLAG
                         *****
 CKSWS:
                 CP
                         FRMCTRH, #0
                                         ;FRAME CTR = 0?
                 JR
                         nz, LOOP
                         FRMCTRL, #0
                 ср
                 JR
                         nz, LOOP
 SCHTOPP:
                 STOP
         TIMER 1 INTERRUPT ROUTINE
 , *****************************
 T1 INT:
                 CALL
                         CKB1
                 ΕI
                                         ; enable interrupt
                 CP
                         RSFLAG, #OFFh
                                         ;RS232 CALL ENABLE FLAG
                 JR
                         nz, BEGINT
```

```
call
                             RS232
                                              ;RS232 I/O
                    push
                             RP
        OR
                             P2,#01000000B
                                              ;set P26 hi
                    NOP
                    AND
                             P2, #10111111b
                                              ;set P26 lo
                               .
*************
         ********FRAME 0 sync pulse on P26*************
                    CP
                             LPCNTR, #00H
                                              ;testing frame sync pulse
                    JR
                             nz, NOSYNC
                                              ;testing frame sync pulse
                    OR
                             P2,#01000000B
                                              ; set frame sync pulse hi
                    JR
                             BEGINT
   ; NOSYNC:
                    AND
                             P2, #10111111b
                                              ;set frame sync pulse lo
                            ******
   BEGINT:
                    INC
                            BITPTR
                                              ;next bit
ı,Çj
                    CP
                            LPCNTR, #00
                                             ; LPCNTR 0 ?
                    JR
                            nz, NEXT
Ę
                    CP
                            BITPTR, #00
                                             ;BITPTR 0 ?
JR
                            nz, NEXT
lug h
                    SUB
                            FRMCTRL, #1
                                             ; DECREMENT FRAME COUNTER
                            FRMCTRH, #0
:2 H
                    SBC
   NEXT:
                    CALL
                            XMT
                                             ;XMT next bit
CP
                            LPCTR, #45
                                             ; nibble 45?
JR
                            nz, CKBP5
   CKBP3:
                    CP
                            BITPTR, #1
įŝ
                    JR
                            z, BP00
iegis
                    IRET
CKBP5:
                    CP
                            BITPTR, #03h
[zzk
                    JR
                            z,BP00
                    IRET
   BP00:
                    LD
                            BITPTR, #OFFH
                                             ; reset bit pointer
                    INC
                            LPCNTR
                                             ;increment nibble pointer
  CK2145:
                    CP
                            LPCNTR, #21
                                             ;lpcntr>20?
                    JR
                            mi, CK6790
                                             ;no
  LP46:
                            LPCNTR, #46
                    CP
                                             ;yes,lpcntr<46
                    JR
                            pl, CK6790
  XMR00:
                    LD
                            xmtreg, #3
                                             ;yes
                    IRET
  CK6790:
                    CP
                            LPCNTR, #67
                                             ;no
                    JR
                            mi,LP91
                   CP
                            LPCNTR, #91
                   JR
                            mi, XMR00
  LP91:
                   CP
                            LPCNTR, #91
                                             ; LPCNTR=91?
                    JR
                            z, LPCTR00
  LPCTROORET:
                   \mathbf{M}\mathbf{T}
                            LPCNTR, #00000001b
                                                     ;LPCNTR bit0=0?
                   JR
                            nz, INCACODE
                   DEC
                            trcptr
                                             ;no
                   LD
                            CODEPTR, trcptr
                   LD
                            xmtreg, @CODEPTR
                   IRET
  INCACODE:
                   INC
                            acodeptr
                                             ;yes
                   LD
                            CODEPTR, acodeptr
                   LD
                            xmtreg, @CODEPTR
                   IRET
```

```
LPCTR00:
               clr
                      LPCNTR
               LD
                      TRCPTR, #SYNCO
               LD
                      acodeptr, #ACODEOBM-1
               LD
                      xmtreg, SYNCO
               LD
                      CODEPTR, #SYNCO
               IRET
       ADD TRINARY NUMBER TO ITSELF ROUTINE
       *****************
 AD3XX:
               ADD
                     x3xabcd,x3xtmp ;add to itself
               ADC
                     x3xabcd1,x3xtmp1
                     x3xabcd2,x3xtmp2
               ADC
               ADC
                      x3xabcd3,x3xtmp3
               ret
 XFER:
               LD
                     x3xtmp,x3xabcd
               LD
                     x3xtmp1,x3xabcd1
               LD
                     x3xtmp2,x3xabcd2
               LD
                     x3xtmp3,x3xabcd3
Æ,
               ret
 ENTR3:
               LD
                    x3xabcd, #03h
               clr
                     x3xabcd1
i.
               clr
                     x3xabcd2
: ###
##
               clr
                     x3xabcd3
1,1,5
               ret
 ENTR3A:
               LD
                     x3xtmp, #03h
                     x3xtmp1
               clr
lagh
               clr
                     x3xtmp2
122
               clr
                     x3xtmp3
               ret
       ******************
        TRANSMIT ROUTINE
        *************
 XMT:
               CP
                     XMTREG, #3
                                          ;BLANK TIME?
               JR
                     z,SBOLO
                                          ;yes
               CP
                     XMTREG, #2
                                          ;force trinary
               JR
                     ule,XMM
                     XMTREG, #2
               ld
                                          ; TWO
 : MMX
               LD
                     XMTREG1, XMTREG
                                         ;no,get xmt code
                     XMTREG1
               COM
                                          ;compliment
               AND
                     XMTREG1, #00000011b
                                          ;mask 2 LSB
               CP
                     XMTREG1, BITPTR
                                          ; compare bitptr to xmtreg
               JR
                     le,SBOHI
 SBOLO:
              AND
                     PO, #11111110b
                                          ;set P00 lo
               RET
 SBOHI:
               OR
                     P0,#00000001b
                                          ;set P00 hi
              RET
 ***********************
 ; WRITE WORD TO MEMORY
 ; ADDRESS IS SET IN REG ADDRESS
 ; DATA IS IN REG MTEMPH AND MTEMPL
 ; RETURN ADDRESS IS UNCHANGED
 WRITEMEMORY:
              push
                     RΡ
                                 ; SAVE THE RP
```

1

```
srp
                        #REGGRP40 ; set the register pointer
                  call
                          STARTB
                                         ; output the start bit
                          serial, #00110000B
                                               ; set byte to enable write
; output the byte
                          SERIALOUT
                  call
                  and
                          csport, #csl
                                                ; reset the chip select
                  call
                          STARTB
                                         ; output the start bit
                  1d
                          serial,#01000000B
                                              ; set the byte for write
                          serial, address
                  or
                                                ; or in the address ; output the byte
                  call
                          SERIALOUT
                  ld
                         serial, mtemph
                                                ; set the first byte to write
                  call
                         SERIALOUT
                                                ; output the byte
                  ld
                         serial, mtempl
                                                ; set the second byte to writ
  е
                  call
                         SERIALOUT
                                                ; output the byte
                  call
                         ENDWRITE
                                         ; wait for the ready status ; output the start bit
                         STARTB
serial
                  call
                  clr
                                         ; set byte to disable write
                  call
                         SERIALOUT
                                                ; output the byte
                  and
                         csport, #csl
200 Mark
                                                ; reset the chip select
                  pop
                                                ; reset the RP
csk
  ; READ WORD FROM MEMORY
  ; ADDRESS IS SET IN REG ADDRESS
  ; DATA IS RETURNED IN REG MTEMPH AND MTEMPL
  ; ADDRESS IS UNCHANGED .
  facts
  READMEMORY:
                 CALL
                        CKB1
                  push
                        RP
zzis
                  srp
                        #REGGRP40
                                       ; set the register pointer
4,1
                 call STARTB ; output the start bit ld serial, #10000000B ; preamble for read
lank.
                  or
                        serial,address
                                                ; or in the address
                        SERIALOUT
SERIALIN
                  call
                                                ; output the byte
                  call
                                                ; read the first byte
                  ld
                        mtemph, serial
                                                ; save the value in mtemph ; read teh second byte
                  call
                         SERIALIN
                  ld
                        mtempl, serial
                                               ; save the value in mtempl
                         csport, #csl
                  and
                                                ; reset the chip select
                 pop
                         RP
  ************
  ; START BIT FOR SERIAL NONVOL
  ; ALSO SETS DATA DIRECTION AND AND CS
  *********************
  STARTB:
                        P2M, #P2M_INIT ; set port 2 mode forcing output mode
                 ld
   data
                 and
                         csport, #csl
                 and
                         clkport, #clock1
                                                        ; start by clearing t
  he bits
                 and
                         dioport,#dol
                         csport, #csh
dioport, #doh
                 or
                                               ; set the chip select
; set the data out high
                 or
                 or
                         clkport, #clockh
                                               ; set the clock
```

```
and
                            clkport, #clock1
                                                     ; reset the clock low
                   and
                            dioport, #dol
                                                     ; set the data low
                   ret
                                                     ; return
  ; END OF CODE WRITE
                                       **********
  ENDWRITE:
                   là
                           P2M, # (P2M INIT+1)
                                                     ; set port 2 mode forcing inp
  ut mode data
                   and
                           csport, #csl
                                                     ; reset the chip select
                                                     ; delay
                   nop
                            csport, #csh
                   or
                                                     ; set the chip select
  ENDWRITELOOP:
                   WDT
                                                     ; kick the dog
                           LPCNTRA, #1
                   cp
                           nz, EWRLP
                   jr
                   call
                           CKB1
  EWRLP:
                   ld
                            temph, dioport
                                                     ; read the port
                            temph, #doh
                                                     ; mask
                   and
                   jr
                            z, ENDWRITELOOP
                                             ; if the bit is low then loop till we
  are done
ų,
                   and
                            csport, #csl
                                                     ; reset the chip select
                   ld
ij.
                            P2M, #P2M INIT
                                             ; set port 2 mode forcing output mode
                   ret
issis
; SERIAL OUT
; OUTPUT THE BYTE IN SERIAL
  SERIALOUT:
                            P2M, #P2M_INIT
                                           ; set port 2 mode forcing output mode
   data
                           templ, #8H
                   ld
                                                     ; set the count for eight bit
SERIALOUTLOOP:
                   rlc
                            serial
                                                     ; get the bit to output into
The carry
                   jr
                           nc, ZEROOUT
                                                     ; output a zero if no carry
  ONEOUT:
                   or
                            dioport, #doh
                                                     ; set the data out high
                   or
                           clkport, #clockh
                                                     ; set the clock high
                   and
                            clkport, #clockl
                                                     ; reset the clock low
                   and
                            dioport, #dol
                                                     ; reset the data out low
                   djnz
                            templ, SERIALOUTLOOP
                                                     ; loop till done
                   ret
                                                     ; return
  ZEROOUT:
                   and
                            dioport, #dol
                                                     ; reset the data out low
                   or
                            clkport, #clockh
                                                     ; set the clock high
                           clkport,#clockl
dioport,#dol
                   and
                                                     ; reset the clock low
                   and
                                                     ; reset the data out low
                   djnz
                            templ, SERIALOUTLOOP
                                                     ; loop till done
                   ret
                                                     ; return
    SERIAL IN
    INPUTS A BYTE TO SERIAL
  SERIALIN:
                   ld
                           P2M, # (P2M INIT+1)
                                                     ; set port 2 mode forcing inp
```

```
ut mode data
                   ld
                           templ, #8H
                                                    ; set the count for eight bit
   SERIALINLOOP:
                   or
                           clkport, #clockh
                                                    ; set the clock high
                   rcf
                                                    ; reset the carry flag
                   ld
                           temph, dioport
                                                   ; read the port ; mask out the bits
                   and
                           temph, #doh
                   jr
                           z, DONTSET
                   scf
                                                   ; set the carry flag
   DONTSET:
                  rlc
                           serial
                                                   ; get the bit into the byte
                   and
                           clkport, #clockl
                                                   ; reset the clock low
                   djnz
                           templ, SERIALINLOOP
                                                   ; loop till done
                   ret
                                                    ; return
  ;
           RS232 DATA ROUTINES
            enter rs232 start with word to output in rs232do
RS232OSTART:
                   clr
                           rsstart
                                                   ; one shot
L.J.
                   ld
                                                   ; set the time delay to 3. mS
                           rs232odelay,#6d
                   clr
                           rs232docount
                                                   ; start with the counter at 0
38
                   and
                                                   ; clear the output
                           RS2320P, #RS2320C
235
                   jr
                           NORSOUT
  RS232:
la se la
                   push
                                                   ; save the rp
                   srp
                           #REGGRP10
                                                   ; set the group pointer
4,]
                           RSSTART, #0FFH
                   ср
                                                   ; test for the start flag
                   jr
                           z, RS232OSTART
RS2320UTPUT:
                           rs232docount,#11d
                   ср
                                                   ; test for last
                   jг
                           nz, RS232R
                           RS2320P, #RS2320S
                   or
                                                   ; set the output idle
                   JR
                           NORSOUT
  RS232R:
                  djnz
                          rs232odelay, NORSOUT
                                                           ; cycle count time de
  lay
                   inc
                           rs232docount
                                                           ; set the count for t
  he next cycle
                  scf
                                                           ; set the carry flag
  for stop bits
                   rrc
                           rs232do
                                                           ; get the data into t
  he carry
                   jr
                           c, RS232SET
                                                           ; if the bit is high
  then set
                  and
                           RS2320P, #RS2320C
                                                           ; clear the output
                   jr
                           SETTIME
                                                   ; find the delay time
  RS232SET:
                  or
                          RS2320P, #RS2320S
                                                           ; set the output
  SETTIME:
                           rs232odelay,#6d
                  ld
                                                   ; set the data output delay
                  tm
                           rs232docount, #00000001b; test for odd words
                          z, NORSOUT
                  jr
                                                           ; if even done
```

```
ld
                            rs232odelay,#7d
                                                     ; set the delay to 7 for odd
                                                              ; this gives 6.5 *.51
  2mS
  NORSOUT:
  RS232INPUT:
                            rs232dicount, #0FFH
                   ср
                                                              ; test mode
                    jr
                            nz, RECEIVING
                                                              ; if receiving then j
  ump
                   tm
                            RS232IP, #RS232IM
                                                              ; test the incoming d
  ata for lo start bit
                   jr
                            nz, NORSIN
                                                              ; if the line is stil
  l idle then skip
                   clr
                            rs232dicount
                                                              ; start at 0
                            rs232idelay, #3
                   ld
                                                              ; set the delay to mi
  RECEIVING:
                   djnz
                            rs232idelay, NORSIN
                                                              ; skip till delay is
II up
123
                   inc
                            rs232dicount
                                                              ; bit counter
1.7%
                            rs232d1count, #10d
                   Ср
                                                              ; test for last timeo
M
                   jr
                            z, DIEVEN
926
                   tm
                            RS232IP, #RS232IM
                                                              ; test the incoming d
  ata
                   rcf
; clear the carry .
                   jr
                            z, SKIPSETTING
                                                     ; if input bit not set skip s
detting carry
                   scf
                                                              ; set the carry
SKIPSETTING:
the memory
                   rrc
                           rs232di
                                                              ; save the data into
sek
                   ld
                           rs232idelay, #6d
                                                              ; set the delay
إ چة'
                           rs232dicount, #00000001b; test for odd
                   tm
123
                   jr
                           z, NORSIN
                                                              ; if even skip
                           rs232idelay, #7
                   ld
                                                              ; set the delay
                   jr
                           NORSIN
  DIEVEN:
                   ld
                           rs232dicount, #0FFH
                                                             ; turn off the input
  till next start
                   ld
                           rscommand, rs232di
                                                              ; save the value
                   clr
                           rsccount
                                                              ; clear the counter
 NORSIN:
                   pop
                                                              ; return the rp
                   ret
                  ********CKB********
  CKB1:
                   WDT
                                                     ; HIT WDT
                   tcm
                           P3, #S1
                                                     ;switch 1 pressed?
                           nz, CKB2
                   jр
                   clr
                           AC19
                                            ; ,#S1B39 yes
                   ld
                           RCP, #RC10B
                                                     ;set rcptr sl
                   RET
 CKB2:
                   tcm
                           P3,#S2
                                                     ;no, switch 2 pressed?
                   jр
                           nz, CKB3
                   ĺď
                           AC19, #S2B39
                                            ;yes
                  ld
                           RCP, #RC20B
                                                    ;set rcptr s2
                  RET
 CKB3:
                  tcm
                           P3, #S3
                                                    ;no, switch 3 pressed?
```

		jp ld ld RET	nz,HELL AC19,#S3B39 RCP,#RC30B	;yes	;set	rcptr	s3
	HELL:	NOP jr STOP	CKB1				
The state of the s	end	FILL FILL FILL FILL FILL FILL FILL FILL					

; T0 SET TO 2uS clear each edge if timer extension times out then clear radio ; T1 set to 1uS for 256 uS roll to turn on the interrupts and to generate the 1 mS  $\,$ 

Bit 35	Bit 37	Bit 39	ID_BIT	Туре
0	0	Add In	0 _	Normal CMD
0	1	Add In	1	Touch code
0	2	Add In	2	Security
1	0	Add in	3	IR Protector
1	1	Key ID	4	Wall control
1	2	Key ID	5	Up Down CMD
2	0	Key ID	6	Up Down Stop
2	1	Don't learn	7	Open Door Indicator
2	2	Don't learn	8	Aux Function

1	NIO	NI_	VOL	MAEN	<b>JORY</b>	MAAD
i	V	IV-	VUL	M⊟N	лонт	MAP

00 01	A1 A1	RA1 RA1	RADIOP5 RADIO1P5
02 03	A2 A2	RC1 RC1	COUNTP5 COUNT1P5
03	A3	RA2	COGNITES
05	A3	RA2	
06	A4	RC2	
07	A4	RC2	
08	A5	RA3	
09	A5	RA3	
0Å	A6	RC3	
0B	A6	RC3	
0C	A7	RA4	
0D	A7	RA4	
0E	<b>A</b> 8	RC4	
0F	<b>A</b> 8	RC4	
10	<b>A</b> 9	RA5	
11	<b>A</b> 9	RA5	
12	A10	RC5	
13	A10	RC5	
14	A11	RA6	
15	A11	RA6	
16	A12	RC6	
17	A12	RC6	
18	В	RA7	
19	В	RA7	
1A	C	RC7	
1B	C	RC7	
1C			ITER 1ST 16 BITS
1D			ITER 2ND 16 BITS
1E		TION FL	
1F			DDRESS LAST WRITTEN
	0XXXX		ABC CODES
	1XXXX	XXXX	D CODES

MirrorB

MirrorC

MirrorD

.equ

.equ

.equ

0DH

0EH

0FH

#### 20-2F OPERATION BACK TRACK 30-3F FORCE BACK TRACE **EQUATE STATEMENTS** 0A2H check\_sum\_value .equ TIMER\_0 .equ 10H TIMER\_0\_EN .equ 03H TIMER\_1\_EN 0CH .equ P01M\_INIT .equ 00000100B ; set mode p00-p03 out P2M\_INIT 00100100B .equ P3M\_INIT .equ 00000011B ; set port3 p30-p33 ANALOG input P01S\_INIT .equ 0000000B P2S\_INIT .equ 00100110B P3S\_INIT 0000000B .equ MONOPER .equ 38D ; MONOSTABLE PERIOD \*4mS 130D RTOPERIOD .equ ; period \*4mS => min 4\* period INTERRUPTS ALL\_ON\_IMR .equ .equ .equ 00111001b ; turn on int for radio RETURN\_IMR 00111001b ; return on the IMR Counter group CounterGroup .equ 00 ; counter group LastM1Match ; last match 1 delay location .equ 05H LastMatch .equ 06H ; last matching code address LoopCount .equ 07H ; loop counter CounterA H80 ; counter translation MSB .equ CounterB 09H .equ CounterC 0AH .equ CounterD OBH .equ ; counter translation LSB MirrorA -.equ 0CH ; back translation MSB

; back translation LSB

learnt

eraset

mono

rto

skipradio

```
loopcount
                        .equ
                                r7
countera
                        .equ
                                r8
counterb
                        .equ
                                r9
counterc
                        .equ
                                r10
counterd
                        .equ
                                r11
mirrora
                        .equ
                                r12
mirrorb
                        .equ
                                r13
mirrorc
                        .equ
                                r14
mirrord
                        .equ
                                r15
```

### ; LEARN MODE SWITCHES AND ERASE

LearnModeGroup	.equ	10H	
SW_B	.equ	LearnModeGroup	;
CmdSwitch	.equ	LearnModeGroup+1	; command switch
LearnDebounce	.equ	LearnModeGroup+2	; learn switch debouncer
LeamTimer	.equ	LearnModeGroup+3	; learn timer
SkipRadio	.equ	LearnModeGroup+4	; flag to skip the radio read
ClearCount	.equ	LearnModeGroup+5	•
EraseTimer	.equ	LearnModeGroup+6	; erase timer
BIT13	.equ	LearnModeGroup+7	•
BIT1P5	.equ	LearnModeGroup+8	,
ID_B	.equ	LearnModeGroup+9	•
LASTBIT	.equ	LearnModeGroup+10	•
PAST_MATCH	.equ	LearnModeGroup+11	•
Mono	.equ	LearnModeGroup+13	•
RadioTimeOut	.equ	LearnModeGroup+14	; radio time out
SwitchSkip	.equ	LearnModeGroup+15	• 5
cmdswitch	.equ	П	;
learndb	.equ	r2	;

# ; LEARN EE GROUP FOR LOOPS ECT

.equ

.equ

.equ

.equ

rЗ

r4

r6

r14

, LearnEeGroup	.equ	20H	:
TempH	.equ	LearnEeGroup	:
TempL	.equ	LearnEeGroup+1	<b>;</b>
Temp	.equ	LearnEeGroup+2	;
COUNT1P5H	.equ	LearnEeGroup+3	; counter value memory
COUNT1P5L	.equ	LearnEeGroup+4	; counter value memory
CMP	.equ	LearnEeGroup+5	•
MTempH	.equ	LearnEeGroup+6	; memory temp
MTempL	.equ	LearnEeGroup+7	; memory temp
MTemp-	.equ	LearnEeGroup+8	; memory temp
Serial	.equ	LearnEeGroup+9	; serial data to and from nonvol memory
Address	.equ	LearnEeGroup+10	; address for the serial nonvol memory
T0Ext	.equ	LearnEeGroup+11	; timer 0 extend dec every T0 int
T4MS	.equ	LearnEeGroup+12	; 4 mS counter

T125MS	.equ	LearnEeGroup+13	; 125mS counter
COUNTP5H	.equ	LearnEeGroup+14	; counter value memory
COUNTP5L	.equ	LearnEeGroup+15	; counter value memory
temph templ temp cmp mtemph mtempl mtemp serial address t0ext t4ms t125ms	.equ .equ .equ .equ .equ .equ .equ .equ	r0 r1 r2 r5 r6 r7 r8 r9 r10 r11 r12 r13	; ; ; ; ; ; ; ; ; memory temp ; memory temp ; serial data to and from nonvol memory ; address for the serial nonvol memory ; timer 0 extend dec every T0 int ; 4 mS counter ; 125mS counter

*******	******	*******	*******
; RADIO GROUP			
,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	******	*******	******
RadioGroup	.equ	30H	;
RTemp	.equ	RadioGroup	; radio temp storage
RTempH	.equ	RadioGroup+1	; radio temp storage high
RTempL	.equ	RadioGroup+2	; radio temp storage low
RTimeAH	.equ	RadioGroup+3	; radio active time high byte
RTimeAL	.equ	RadioGroup+4	; radio active time low byte
RTimelH	.equ	RadioGroup+5	; radio inactive time high byte
RTimeIL	.equ	RadioGroup+6	; radio inactive time low byte
RadioP5H	.equ	RadioGroup+7	; .5 code storage
RadioP5L	.equ	RadioGroup+8	; .5 code storage
PointerH	.equ	RadioGroup+9	;
PointerL	.equ	RadioGroup+10	•
AddValueH	.equ	RadioGroup+11	į
AddValueL	.equ	RadioGroup+12	•
RadioC	.equ	RadioGroup+13	; radio word count
Radio1P5H	.equ	RadioGroup+14	; 1.5 code storage
Radio1P5L	.equ	RadioGroup+15	; 1.5 code storage
rtemp	.equ	r0	; radio temp storage
rtemph	.equ	r1	; radio temp storage high
rtempl	.equ	r2	; radio temp storage low
rtimeah	.equ	<b>r</b> 3	; radio active time high byte
rtimeal	.equ	r4	; radio active time low byte
rtimeih	.equ	r5	; radio inactive time high byte
rtimeil	.equ	r <u>6</u>	; radio inactive time low byte
radiop5h	.equ	r <b>7</b>	; radio .5 code storage
radiop5l	.equ	<b>18</b>	; radio .5 code storage
pointerh	.equ	r9	;
pointerl	.equ	r10	;
addvalueh	.equ	r11	;
addvaluel	.equ	r12	;
radioc	.equ	r13	; radio word count
radio1p5h	.equ	r14	; radio 1.5 code storage
radio1p5l	.equ	r15	; radio 1.5 code storage
			<del>-</del>

# ; Check sum group with past radio data

CheckGroup	.equ	40H	
check_sum	.equ	r0	; check sum pointer
rom_data	.equ	n	
test_adr_hi	.equ	12	
test_adr_lo	.equ	r3	
rflag	.equ	r4	
test_adr	.equ	rr2	
pradioa	.equ	r6	
pradiob	.equ	r7	
pradioc	.equ	r8	
pradiod	.equ	r9	
pradioe	.equ	r10	
pradiof	.equ	r11	
pradiog	.equ	r12	
pradioh	.equ	r13	
Check Sum	.equ	CheckGroup+0	; check sum reg for por
Rom_Data	.equ	CheckGroup+1	; data read
RFlag	.equ	CheckGroup+4	; radio flags
RInFilter	.equ	CheckGroup+5	; radio input filter
PRadioA	.equ	CheckGroup+6	; past recieved value
PRadioB	.equ	CheckGroup+7	; past recieved value
PRadioC	.equ	CheckGroup+8	; past recieved value
PRadioD	.equ	CheckGroup+9	; past recieved value
PRadioE	.equ	CheckGroup+0AH	; past recieved value
PRadioF	.equ	CheckGroup+0BH	; past recieved value
PRadioG	.equ	CheckGroup+0CH	; past recieved value
PRadioH	.equ	CheckGroup+0DH	; past recieved value
	•		

; Timer group with rs232 data

TimerGroup	.equ	50H
rs232do	.equ	r5
rs232di	.equ	r6
rscommand	.equ	r7
rs232docount	.equ	r8
rs232dicount	.equ	r9
rs232odelay	.equ	110
rs232idelay	.equ	r11
rs232ccount	.equ	r12
rs232page	.equ	r13
rsccount	.equ	r14
rsstart	.equ	r15
<del>-</del>	-	
RADIO_CMD	.equ	Time

RADIO\_CMD .equ TimerGroup+0H
TaskSwitch .equ TimerGroup+2H
SysDisable .equ TimerGroup+3H
ADD2 .equ TimerGroup+4H

; radio command

; system disable timer

RS232DO RS232DI RSCommand RS232DoCount RS232DiCount RS232ODelay RS232IDelay RS232CCount RS232Page RSCount RSStart TestVal	.equ .equ .equ .equ .equ .equ .equ .equ	TimerGroup+5 TimerGroup+6 TimerGroup+7 TimerGroup+8 TimerGroup+9 TimerGroup+10 TimerGroup+11 TimerGroup+12 TimerGroup+13 TimerGroup+14 TimerGroup+15 TimerGroup+16	; rs232 byte counter ; rs232 start flag ; test value
STACKTOP STACKEND	.equ .equ	127D 060H	; start of the stack '; end of the stack
RS232OS RS232OC RS232OP RS232IP RS232IM csh csl clockh clockl doh dol csport dioport clkport	.equ .equ .equ .equ .equ .equ .equ .equ	00000100B 11111011B P0 P3 00000010B 0000001B 11111110B 00000010B 111111101B 00000001B 11111110B P0 P2 P0	; RS232 output bit set ; RS232 output bit clear ; RS232 output port ; RS232 input port ; RS232 mask ; chip select high for the 93c46 ; chip select low for 93c46 ; clock high for 93c46 ; clock low for 93c46 ; data out high for 93c46 ; data out low for 93c46 ; chip select port ; data i/o port ; clock port
WDT	.macro	5fh	
WDH	.macro .byte .endm	4fh	
Fill	.macro .byte .endm	0FFH	
,*************************************	******	*************	******
Interrup	t Vector	Table	*******
org -	0000H		
	.word .word .word	RadioNegInt 000CH 000CH	;IRQ0 P3.2 n ;IRQ1, P3.3 ;IRQ2, P3.1

```
; USE P3.0 FROM 28 PIN
                       .word
                               TimerZeroInt
                                                       ;IRQ4, T0
                       .word
                               TimerOneInt
                                                      ;IRQ5, T1
                        .page
                        000CH
               .org
 WATCHDOG INITILIZATION
start:
START:
               di
                                                      ; turn off the interrupt for init
               WDH
               WDT
                                                      ; kick the dog
          Internal RAM Test and Reset All RAM = mS
               srp
                       #0F0h
                                                      ; point to control group use stack
               ld
                       r15,#4
                                                      ;r15= pointer (minimum of RAM)
write_again:
               WDT
                                                      ; KICK THE DOG
               ld
                       r14,#1
write_again1:
               ld
                       @r15,r14
                                                      ;write 1,2,4,8,10,20,40,80
                       r14,@r15
               ср
                                                      ;then compare
               jr
                       ne,system_error
               rl
               ir
                       nc,write_again1
               clr
                       @r15
                                                      ;write RAM(r5)=0 to memory
               inc
                       r15
               ср
                       r15,#7FH
               jr
                       ult,write_again
```

.word

RadioPosInt

;IRQ3, P3.2 p FOR EMULATION

```
Checksum Test
CheckSumTest:
               srp
                      #CheckGroup
               ld
                      test_adr_hi,#07H
               ld
                      test_adr_lo,#0FFH
                                                    ;maximum address=fffh
add_sum:
               WDT
                                                    ; KICK THE DOG
               ldc
                      rom_data,@test_adr
                                                    read ROM code one by one
               add
                      check_sum,rom_data
                                                    ;add it to checksum register
               decw
                      test_adr
                                                    increment ROM address;
                      nz,add_sum
               jr
                                                    ;address=0 ?
               ср
                      check_sum,#check_sum_value
               jr
                      system_ok
                      z,system_ok
                                                    ;check final checksum = 00 ?
system_error:
               and
                      P2,#11011101B
                                                    ; turn on the LED to indicate fault
```

	ld	P2M,#P2M_INIT	; turn on the LED to indicate fault
	jr	system_error	
system_ok:	.byte	256-check_sum_value	
	WDT		; kick the dog
	srp	#LearnModeGroup	; set the group
	ld ld ld ld ld	eraset,#0FFH CmdSwitch,#0FFH learnt,#0FFH learndb,#0FFh RSCommand,#0FFH RS232DoCount,#11D	; set the erase timer ; set the switch debouncer ; set the learn timer ; set the learn debounce ; turn off the rs232 command ; turn off the rs232 output
,		***********************	******
; STACK INITII ; SetStack:	LIZATIO	:N ************************************	*******
SelSlack.	-1-	054	
	cir Id	254 255,#STACKTOP	; set the start of the stack
;*****; TIMER INITIL		**************************************	*****
,*************************************		******************************	******
	ld ld clr clr ld	PRE0,#00001001B PRE1,#00000111B T0 T1 TMR,#00001111B	; set the prescaler to / 2 for 8Mhz ; set the prescaler to / 1 for 8Mhz ; set the counter to count FF through 0 ; set the counter to count FF through 0 ; turn on the timers and load
; PORT INITILI		**************************************	******
, FOR FINITIE	******	******************************	********
	id id id id id	P0,#P01S_INIT P2,#P2S_INIT P3,#P3S_INIT P01M,#P01M_INIT P3M,#P3M_INIT P2M,#P2M_INIT+1	; RESET all ports ; ; set mode ; set port3 p30-p33 input analog mode ; set port 2 mode
; MEMORY INI		ion	
,#####################################	*****	**********************	*******
	ld call	Address,#3EH ReadMemory	; set non vol address to UNUSED ; read the value to INIT

SetInterrupts			
-	ld	IPR,#0000001B	; set the priority to timer
	ld cir	IMR,#ALL_ON_IMR IRQ	; turn on the interrupt
	CII	ing	; CLEAR IRQ'S
.*************************************	******	*************	*********
; MAIN LOO	P *******	***********	******
, MainLoop:			
	ei		; enable interrupt
	and	P2,#01111111b	; turn off the flag
	WDT	DOALS SDOALS INST	; kick the dog
	ld	P01M,#P01M_INIT	; set mode
	ld ld	P3M,#P3M_INIT P2M,#P2M_INIT+1	; set port3 p30-p33 input analog mode
	iu	1 2141,#1 2141_11411+1	; set port 2 mode
TestRS232:	call	LEARN	; do the learn switch
1631110202.	srp	#TimerGroup	
	ср	rsstart,#0FFH	; test for starting a transmission
	ir	z,skiprs232	; if starting a trans skip
	ср	rscommand,#0FFH	; test for the off mode
	jr	z,skiprs232	,
	ср	rs232docount,#11d	; test for output done
	jr	nz,skiprs232	; if not the skip
	ср	rscommand,#30H	; test for switch data
•	jr ola	nz,TEST34	
)	cir	rs232do	; clear the data
• •	ср	LearnDebounce,#0FFH	; test switch one
	jr	nz,SW1OUT	
OMA OUT	or	rs232do,#0000001B	; set the marking bit
SW1OUT:		0 10 11 11 110 111	
i	cp ir	CmdSwitch,#0FFH nz,SW2OUT	; test switch 2
•	jr or	rs232do,#0000010B	;
SW2OUT:	O1	1923200,#00000010B	; set the marking bit
	ср	LearnTimer,#0FFH	; test for learn 1
, •	jr	nz,L1OUT	, 1001 10. 1001
•	or	rs232do,#00001000B	; set the marking bit
L1OUT:			•
ΓEST34:	jr	VacSwOpen	;
163134.	ср	rscommand,#34H	; test for page 0
	jr	nz,TEST35	, test for page o
_	ld	rs232page,#00H	;
	jr	RS232PageOUT	•
EST35:	-	-	
	ср	rscommand,#35H	; test for page 1
	jr	nz,TEST38	

	ندا	m=000;;	
	ld	rs232page,#10H	;
RS232Page(	OUT:		
	ld	SkipRadio,#0FFH	; set the skip radio flag
	dec	SwitchSkip	; turn off the switch testing for port
		•	; direction control
	ld	Address,rsccount	; find the address
	rcf		, and the address
	rrc	Address	•
	or	Address,rs232page	•
	call	ReadMemory	, road the data
	ld	rs232do,MTempH	; read the data
	tm	rsccount,#01H	r do ná vyhlada tv. d.
	jr	z,RPBYTE	; test which byte
	j. Id	rs232do,MTempL	
RPBYTE:	10	15252do, WTempt	
5112.	cn	recognint #1EU	
	ср jp	rsccount,#1FH nz,STARTOUT	; test for the end
LASTRPM:	clr		
VacSwOpen:	CII	rsccount	; reset the counter
vacowopen.	-4		
	dec	rsstart	; set the start flag
	ld	rscommand,#0FFH	; turn off command
okingo 000.			; return
skiprs232:	•	01/1070	
	jp	SKIPRS232	
TEST38:			
150130.			
	ср	rscommand,#38H	; test memory
	jr	nz,SKIPRS232	-
	ld	rs232do,#0FFH	; flag set to error to start
	srp	#LearnEeGroup	•
	dec	SwitchSkip	; skip testing the switches
	ld	SkipRadio,#0FFH	; set the skip radio flag
	ld	mtemph,#0FFH	; set the data to write
	call	WRITEALL	; write all the words
	call	TESTALL	; test all memory
	ld	mtemph,#000H	; set the data to write
	call	WRITEALL	; write all memory
	call	TESTALL	; test for the data retension
CLEARALL:			,
	call	CLEARCODES	; reset the memory for code
	cir	RS232DO	; flag all ok
MEMORYER	ROR:		, mag am on
	ld	RSCommand,#0FFH	; turn off command
STARTOUT:		-,	, tarri ori command
	inc	rsccount	; set to the next address
	dec	RSStart	; set the start flag
			, out the start hay
SKIPRS232:			
	cir	SwitchSkip	: cloor the oldin available at the
	clr	SkipRadio	; clear the skip switches flag
			; clear the skip radio flag
	srp	#LearnModeGroup	
	<b>○. P</b>	" -carminode Group	;
SINGLE:			
	ср	mono,#MONOPER	Annak from N
	<b>~P</b>	ONO,#WONOPER	; test for the period

ClearRadioTimeout:

call

jр

push

ClearCounter

ClearRadio

RP

```
; if not then test constant output
                and
                        P2,#11110111b
                                                       ; clear the output
                ld
                        mono,#0FFH
TESTCONS:
                di
                ср
                        rto,#RTOPERIOD
                                                       ; test for the timeout
                jr
                        ult,SIGDONE
TurnOffOutput:
                and
                        P2,#11101111b
                                                       ; clear the output
                ld
                        rto,#0FFH
SIGDONE:
TOGGLE:
                jp
                        MainLoop
                                                      ; loop forever
WRITEALL:
                ld
                       mtempl,mtemph
               ld
                       TestVal,mtemph
                cir
                       address
                                                      ; start at address 00
WRITELOOP1:
                WDT
                call
                       WRITEMEMORY
               inc
                       address
                                                      ; do the next address
               ср
                       address,#40H
                                                      ; test for the last address
                       nz,WRITELOOP1
               jr
               ret
TESTALL:
               clr
                       address
                                                      ; start at address 0
READLOOP1:
               WDT
               call
                       ReadMemory
                                                      ; read the data
               ср
                       mtemph,TestVal
                                                      ; test the value
               jp
                       nz,MEMORYERROR
                                                      ; if error mark
               ср
                       mtempl,TestVal
                                                      ; test the value
               jp
                       nz, MEMORYERROR
                                                      ; if error mark
               inc
                       address
                                                      ; set the next address
               ср
                       address,#40H
                                                      ; test for the last address
               jr
                       nz,READLOOP1
               ret
Timer 0 interrupt
TimerZeroInt:
               ср
                      T0Ext.#00
                                                     ; test for the roll
               jr
                      z,ClearRadioTimeout
                                                     ; if at the roll time out
               dec
                      T0Ext
```

ult,TESTCONS

A-30

; decrement the time extension

; for the Clear radio code segment

; clear the counter

; clear the radio data

```
Radio interrupt from a edge of the radio signal
 RadioNegInt:
                 and
                         IMR,#11111110b
                                                         ; turn off the interrupt for 256uS
                 ld
                         RTemp,#0000001B
                                                         ; mark which edge
                         RadioEdge
                 jr
 RadioPosInt:
                 and
                         IMR,#11110111b
                                                         ; turn off the interrupt for 256uS
                         RTemp,#0000000B
                 ld
                                                         ; mark which edge
                 į٢
                         RadioEdge
 RadioEdge:
                         RP
                 push
                                                         ; save the reg pair
                         #RadioGroup
                 srp
                                                         ; set the register pointer
                 ld
                         rtemph,T0Ext
                                                         ; read the upper byte
                 ld
                         rtempl,T0
                                                         ; read the lower byte
                 tm
                         IRQ,#00010000b
                                                         ; test for a pending timer interrupt
                 jr
                         z,RIncDone
                                                         ; done
                 tm
                         rtempl,#10000000b
                                                         ; test for the rollover
                 jr
                         z,RIncDone
                                                         ; if not the rolled value skip inc
                 dec
                         rtemph
                                                         ; increase the timer msb
 RincDone:
                 call
                         ClearCounter
                                                         ; clear the counter
RTimeOk:
                com
                         rtemph
                                                         ; flip to find the period
                com
                         rtempl
RTimeDone:
                ср
                        rtemp,#0
                                                         ; test the port for the edge
                        z,ActiveTime
                jr
                                                         ; if it was the active time then branch
InActiveTime:
                Ср
                        RInFilter,#0FFH
                                                         ; test for active last time
                jr
                        z,GolnActive
                                                         ; if so continue
                jr
                        RADIO_EXIT
                                                         ; if not the return
GolnActive:
                clr
                        RInFilter
                                                         ; set flag to inactive
                ld
                        rtimeih,rtemph
                                                         ; transfer the period to inactive
                ld
                        rtimeil,rtempl
                jr
                        RADIO EXIT
                                                        ; return
ClearCounter:
                ld
                        TMR,#00001000b
                                                        ; turn off timer 0
                ld
                        TMR,#00001001b
                                                        ; load t0
                ld
                        TMR,#00001000b
                ld
                        TMR,#00001010b
                                                        ; restart the timer
                ld
                        T0Ext,#0FFH
                                                        ; reset the timer
                and
                        IRQ,#11100110b
                                                        ; turn off pending int
                ret
ActiveTime:
                ф
                        RInFilter,#00H
                                                        ; test for active last time
                jr
                        z,GoActive
                                                        ; if so continue
                        RADIO_EXIT
                                                        ; if not the return
GoActive:
```

	id Id Id	RInFilter,#0FFH rtimeah,rtemph rtimeal,rtempl	; transfer the period to active ;
GotBothEdges:	ei cp	radioc,#0	; enable the interrupts ; test for the blank timing
	jr inc cp	nz,INSIG radioc rtimeih,#30h	; if not then in the middle of signal ; set the counter to the next number ; test for the min 24.5 mS
	jr cp	ult,ClearJump rtimeah,#00h	; if not then clear the radio ; test first the min sync
•	jr cp	nz,SyncOk rtimeal,#80H	; first byte 00 if not great enough ; test for 256uS min
SyncOk:	jr	ult,ClearJump	; if less then clear the radio
	cp jr	rtimeah,#9h uge,ClearJump	; test for the max time 4.6mS ; if not clear
SETP5:	co.	rtimeah,#02h	; test for 1.5 vs .5
P5MSFLAG:	cp jr	uge,O1P5MSFLAG	; set the 1.5 flag
POMOFLAG:	or	RFlag,#01000000b	; set the 0.5ms memory flag
	cir cir	radiop5h radiop5l	; clear the memory :
	clr	COUNTP5H	; clear the memory
O4DEMOE! AO	clr jr	COUNTP5L DONESETP5	; ; do the 2X
O1P5MSFLAG	and	RFlag,#10111111b	; set the 1.5ms memory flag
	clr cir	radio1p5h radio1p5l	; clear the memory
	clr clr	COUNT1P5H COUNT1P5L	; clear the memory
DONESETP5: RADIO_EXIT:			
	pop iret	rp	; done return
ClearJump:	or	P2,#10000000b	; turn of the flag bit for clear radio
	jp	ClearRadio	; clear the radio signal
INSIG:		day hasan	
	cp jr	rtimeih,#0AH uge,ClearJump	; test for the max width 5.16 ; if too wide clear
	cp jr	rtimeih,#00h nz,ISigOk	; test for the min width ; if greater then 0 then signal ok
•	ф	rtimeil,#080h	; test for 256us min
lSigOk:	jr	ult,ClearJump	; if not then clear the radio
·	cp ;r	rtimeah,#0AH uge,ClearJump	; test for the max width
	jr cp	rtimeah,#00h	; if too wide clear ; if greater then 0 then signal ok

ASigOk:	jr cp jr	nz,ASigOk rtimeal,#080h ult,ClearJump	; if too narrow clear ; test for 256us min ; if not then clear the radio
	sub sbc	rtimeal,RTimeIL	; find the difference
POSDIFF2:	tm jr jr	rtimeah,rtimeih rtimeah,#10000000b nz,NEGDIFF2 POSDIFF2	; find out if neg ; use 1 for ABC or D
	cp jr jr	rtimeah;#01H ult,O1PMS O1P5MS	; test for 1.5/1 ; mark as a 1
NEGDIFF2:			
O1P5MS:	com cp jr jr	rtimeah rtimeah,#01H ult,O1PMSC P5MSC	; invert ; test for 1/.5 ; mark as a .5
	ld jr	BIT1P5,#2h GOTB1P5	; set the value
O1PMSC:	00 m	milima ala	<u>.</u>
O1PMS:	com	rtimeah	; invert
P5MSC:	ld jr	BIT1P5,#1h GOTB1P5	; set the value
GOTB1P5:	com Id	rtimeah BIT1P5,#0h	; invert ; set the value
,	clr cir clr	rtimeah rtimeal rtimeih	; clear the time
	clr	rtimeil	
ADDB1P5:	ei		; enable interrupts
	tm jr	RFlag,#01000000b nz,RCP5INC	; test for radio p5/ 1p5 ;
RC1P5INC:			
	tm jr	radioc,#00000001b z,COUNT1P5INC	; test for even odd number ; if odd number counter
Radio1P5INC:			; else radio
Radio1P5R:	cp jr	radioc,#15D uge,SPECIAL_BITS	; test the radio counter for the specials ; save the special bits seperate
- · · · · · · · · · · · · · · · · · · ·	ld ld jr	pointerh,#Radio1P5H pointerl,#Radio1P5L AddAll	; get the pointer ;
SPECIAL_BITS	s: cp	radioc,#15d	the state of the s
	jr clr	nz,SKIP_ID_ZERO ID_B	; test for the first special ; if not then skip zeroing ; else clear the id bits

SKIP ID ZERO:						
SKIP_ID_ZEN	_iD_ZERO: cp radioc,#19d jr z,SWITCHID		; test for the switch id ; if so then branch			
CIMITOLIID	ld add add add jr	rtemph,ID_B ID_B,rtemph ID_B,rtemph ID_B,BIT1P5 Radio1P5R	; save the special bit ; *3 ; *3 ; add in the new value			
SWITCHID:	ld cp jr clr jr	SW_B,BIT1P5 ID_B,#03d ule,Radio1P5R BIT1P5 Radio1P5R	; save the switch ID ; test for the add in values ; add in if 3 < ; else dont add in			
RCP5INC:	tm jr	radioc,#0000001b z,COUNTP5INC	; test for even odd number ; if odd number counter			
RadioP5INC:	ld ld jr	pointerh,#RadioP5H pointerl,#RadioP5L AddAll	; else radio ; get the pointer ;			
COUNT1P5IN	ld ld jr	pointerh,#COUNT1P5H pointerl,#COUNT1P5L AddAll	; get the pointer ;			
	ld ld jr	pointerh,#COUNTP5H pointerl,#COUNTP5L AddAll	; get the pointers ;			
AddAll:	ld ld ld	rtemph,@pointerh rtempl,@pointerl addvalueh,@pointerh addvaluel,@pointerl	; get the value ; ; get the value ;			
	add adc add adc add adc	addvaluel,rtempl addvalueh,rtemph addvaluel,rtempl addvalueh,rtemph addvaluel,BIT1P5 addvalueh,#00h @pointerh,addvalueh	; add x2 ; add x3 ; add in new number ; save the value			
ALLADDED:	ld inc	@pointerf,addvaluel radioc	; increase the counter			
· , •	and cp jp tm	RFlag,#11011111B radioc,#21D nz,RRETURN RFlag,#00010000B	; clear the bit for 10 bits ; test for 20 ; if not then return ; test flag 20 bit code			

FIRST20:	jr	nz,KNOWCOD	E	; if the second 20 bits received
1110120.	or	RFlag,#000100	IOOR	; set the flag
	cir	radioc	000	; clear the radio counter
	jp	RRETURN		; return
GOT20CODE:	)P	THILIOIN		, return
GOTZOOODE.	ср	ID B,#07d		; test for the don't use ones
	jp	uge,ClearRadio	`	: clear don't use ones
	ср	ID B,#04d	•	; test for the don't add in ones
	ir	uge,KNOWCO	DE	; if so then don't add in
	add	COUNT1P5L,S		; add in switch id
	adc	COUNT1P5H,#		·
KNOWCODE:	440	0.00		,
.*************************************	******	**********	**********	************************
; Translate the	counter	back to normal		
; start		_		
; Counte	erA	CounterB	CounterC	CounterD
; 00		00	Count1P5H	Count1P5L
; MirrorA	١	MirrorB	MirrorC	MirrorD
; 00		00	CountP5H	CountP5L *
,	******	*********	************	
	0.50	#CountorCustor		s and the municipality
	srp clr	#CounterGroup	,	; set the group
	clr	countera		; clear the counter Msb value
		counterb	IT4DELI	, Cotthe velve to severity 5
	ld	counterc,COUN		; Set the value to count1p5
	ld cir	counterd,COU	MITPOL	; Cot the primer (terms now for now)
	cir	mirrora		; Set the mirror (temp reg for now)
	ld	mirrorb	rden -	; to countp5
	ld	mirrorc,COUNT mirrord,COUNT		,
	call	AddMirrorToCo		; find country * 2010 , counting
	ld	loopcount,#3	unei	; find countp5 * 3^10 + count1p5
	call	RotateMirrorAd	И	•
	ld	loopcount,#2	N	•
	call	RotateMirrorAd	ld	•
	ld	loopcount,#2	u	•
	call	RotateMirrorAc	ы	•
	ld	loopcount.#2	···	•
	call	RotateMirrorAd	Ы	•
	ld	loopcount,#1	· ·	•
	call	RotateMirrorAd	ki	•
	ld	loopcount,#3		•
	call	RotateMirrorAd	И	•
	ld	loopcount,#1		•
	call	RotateMirrorAd	Ы	•
	ld	loopcount,#1		•
	call	RotateMirrorAc	ld	•
			<del></del>	,
MirrorTheCoun	iter:			
-	call	MirrorCounter		; mirror the counter
CounterCorrect	ted:			
	ср	SkipRadio,#0F	FH	; test for the skip radio flag
	jp	z,ClearRadio		; if active do not test the cpde
	ср	LearnTimer,#0	FFH	; test for in learn mode

STORECODE	. jp	z,TESTCODE	; if not in learn the test the code
DCODESTOR			
DCODESTOR		DDodio A radio 1 nEh	: tost all 8 mamanus for a match
	cp :-	PRadioA,radio1p5h	; test all 8 memorys for a match ; if no match skip
	jr	nz,PP_NOT_M_D PRadioB,radio1p5l	; test all 8 memorys for a match
	cp :-	nz,PP_NOT_M_D	; if no match skip
	jr	PRadioC,radiop5h	; test all 8 memorys for a match
	cp :-	nz,PP NOT M D	; if no match skip
	jr	PRadioD,radiop5l	; test all 8 memorys for a match
	cp ;-	nz,PP_NOT_M_D	; if no match skip
	jr CD	PRadioE,MirrorA	; test all 8 memorys for a match
	cp ir	nz,PP NOT M D	; if no match skip
	jr CD	PRadioF,MirrorB	; test all 8 memorys for a match
	cp ir	nz,PP_NOT_M_D	; if no match skip
	jr CD	PRadioG,MirrorC	; test all 8 memorys for a match
	ср jr	nz,PP_NOT_M_D	; if no match skip
	-	PRadioH, MirrorD	; test all 8 memorys for a match
	cp ir	nz,PP_NOT_M_D	; if no match skip
MatchedForSt		112,11 _1101_111_12	, it no materi stap
Materical Crot	srp	#LearnEeGroup	
	call	TESTMATCH	; test for a matching code
	cp cp	address,#0FFH	; test for a match
	jr	nz,WRITEAGAIN	: if so store AGAIN for counter
	j. Id	address,#1FH	; set the address
	call	ReadMemory	read the value
	add	mtemph,#4d	; find the next address
	ф	mtemph,#1CH	; test for out of range
	ir	ult,GOTDADDRESS	:
	clr	mtemph	•
GOTDADDRE			,
	ld	mtempl,mtemph	:
	ld	address,#1FH	; store the new address
	call	WRITEMEMORY	•
	ld	address,mtemph	; set the code address to write
	call	WRITE_D_CODE	; output the D code
	jr	NOWRITESTORE	; reset the learn mode
	•		
WRITEAGAIN	<b>l</b> :		
	call	WRITE_D_CODE	; output the D code
NOWRITEST	ORE:		
	or	P2,#0000010B	; turn off the LED for flashing
	ld	LearnTimer,#0FFH	; turn off the learn mode
	clr	RadioTimeOut	; disable command from learn
	jr	ClearRadio	; set for the next code
	_		
PP_NOT_M_I		<b></b>	
	ld	PRadioA,radio1p5h	; save the present into the past
-	ld	PRadioB,radio1p5l	; save the present into the past
	ld	PRadioC,radiop5h	; save the present into the past
	ld	PRadioD,radiop5I	; save the present into the past
	ld	PRadioE, MirrorA	; transfer the value
	ld	PRadioF,MirrorB	

1

	ld Id	PRadioG,MirrorC PRadioH,MirrorD	
			; reset radio
********	*******	*********	******
; Clear interrup	ot ********	*******	
, ClearRadio:			
SKIPIRTO:	tm jr clr	RFlag,#0000001B z,SKIPiRTO RadioTimeOut	; test for receiving without error ; if flag not set then donot clear timer ; clear radio timer
	clr clr	RadioC RFlag	; clear the radio counter ; clear the radio flags
RRETURN:	pop iret	RP	; reset the RP ; return
***********	******	*************	**********
; rotate mirror	LoopCo	unt *2 then add	*************
RotateMirrorAd	id:		
	rcf rlc	mirrord	; clear the carry
	rlc	mirrorc	; :
	rlc	mirrorb	;
	rlc	mirrora	•
.*********	djnz	loopcount,RotateMirrorAdd	; loop till done
; Add mirror to	counter	**********	********************************
, AddMirrorToCo	unter:		
	add	counterd,mirrord	;
•	adc	counterc,mirrorc	,
	adc	counterb,mirrorb	;
>	adc ret	countera,mirrora	•
,*************************************	******	*********	*************
: Add mirror to	counter	*********	*************
MirrorCounter:			
MirrorLoop:	ld	loopcount,#32d	; set the number of bits
	rrc	countera	; move the bits
	rrc	counterb	. Hove the bits
	rrc	counterc	
	rrc	counterd	
	rlc	mirrord	
	rlc	mirrorc	
-	rlc	mirrorb	
	rlc	mirrora	
			loop for all the bits
	ret		•

***********	******	*****	********
; Test the radio code for matching			•
**********	******	***************	*******
TESTCODE:			
	and	P2,#11111101B	; turn on the LED for flashing
	srp	#LearnEeGroup	, terri en are en are
	call	TESTMATCH '	; test the code for a match
	or	P2,#0000010B	; turn off the LED for flashing
	ф	Address,#0FFH	; test for no match
	jp	z,TEST_TC_SEC	; if no match try touchcode and sec
D_CODE_MA	TCH.		, , , , , , , , , , , , , , , , , , , ,
	ф	RadioTimeOut,#0FFH	tool for the times.
	jr	z,NewCode	; test for the timeout
	cp cp	LastM1Match,Address	; if timer inactive then look for a new
	jr	nz,NewCode	; test for the same address as the past
	clr	RadioTimeOut	; if not then test for a new code
	jp	ClearRadio	; reclear the timer
NewCode:	אנ	Olean ladio	; and update the past
	srp	#CheckGroup	; set the rp
	call	TESTCOUNTER	; test the counter for in range
	ф	CMP,#00	test for a metables value
	jp	z,ClearRadio	; test for a matching value
	ср	CMP,#0AAH	; if the same then clear the radio
	jr	z,GOT D CMD	; test for counter in range
	, cp	CMP,#07FH	; got a command save radio counter
	jr	z,UPDATE_PAST	; test for outside of - window
	•	PAST_MATCH,Address	; if so skip resync
	cp ir	nation, Address	; test for the same address as the past
	jr Id	nz,UPDATE_PAST	; if not then update the past value
	ld	pradioa,MirrorA	; transfer the value
	ld	pradiob,MirrorB	
	ld	pradioc, MirrorC	
	sub	pradiod,MirrorD	
		pradiod,pradioh	•
	sbc	pradioc,pradiog	
	sbc	pradiob,pradiof	
	sbc	pradioa,pradioe	; find the difference
	ф	pradioa,#00	; test for less then 4 away
	jr	nz,UPDATE_PAST	; if not then update the past
	ф	pradiob,#00	
	jr	nz,UPDATE_PAST	; if not then update the past
	.cp	pradioc,#00	•
	jr	nz,UPDATE_PAST	; if not then update the past
	ср	pradiod,#00	; test for the zero case
	jr	z,UPDATE_PAST	•
	ф	pradiod,#04d	
COT D OUD	jr	ugt,UPDATE_PAST	; if not then update the past
GOT_D_CMD:			,
	cali	STORE_D_COUNTER	; save the new counter value
D DADIO CO			
D_RADIO_CO			
* * * * * * * * * * * * * * * * * * * *	<b>с</b> р	SysDisable,#32d	; test for 4 seconds
	jr	ult,TEST_TC_SEC	; if not test to and sec
		Dedut o	
	ф	RadioTimeOut,#RTOPERIOD	; test for first reception

· view

	_		
	jr <sub>.</sub>	ult,NOTP3A	; if second reception skip t and mono
•	clr	Mono	; clear the monostable
	or	P2,#00011000B	; turn on the constant
	xor	P2,#01000000B	; toggle the T output
NOTP3A:	clr	RadioTimeOut	; clear the timer
NOTP3:			,
NOTP3S:			
14011 30.	ì	TEST_TC_SEC	; test tc and sec
	jr	1231_10_320	, lest ic and sec
NOTHELLOA	.TOU		
NOTNEWM			
•	id	LearnTimer,#0FFH	; set the learn timer "turn off"
	jр	ClearRadio	; clear the radio
UPDATE_PA	AST:		
_	ld	PAST_MATCH,Address	; save the past address
	ld	pradice,MirrorA	; transfer the value
	ld	pradiof,MirrorB	, adibioi dio valuo
•			
	ld	pradiog,MirrorC	
	ld	pradioh, Mirror D	
	jp	ClearRadio	; reset the radio
**********	******	*******	************
, Wal	know the	code does not match but if it w	as our touch code
			as our touch code
, 01.50	ecurity lia	insmitter update the counter	
		*********************	
•	*********	*************	*********
,		************************	**********
TEST_TC_S	SEC:	****************************	**********
TEST_TC_S	SEC: srp	#LearnEeGroup	
TEST_TC_S		#LearnEeGroup ID_B,#1d	; test for the touch code
TEST_TC_S	srp cp	ID_B,#1d	; test for the touch code
; TEST_TC_S	srp cp jr	ID_B,#1d z,TC_SEC	; test for the touch code ; jump if so
TEST_TC_S	srp cp jr cp	ID_B,#1d z,TC_SEC ID_B,#2d	; test for the touch code ; jump if so ; test for the security transmitter
, TEST_TC_S	srp cp jr cp jr	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC	; test for the touch code ; jump if so
	srp cp jr cp	ID_B,#1d z,TC_SEC ID_B,#2d	; test for the touch code ; jump if so ; test for the security transmitter
TEST_TC_S	srp cp jr cp jr jp	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so
TC_SEC:	srp cp jr cp jr	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC	; test for the touch code ; jump if so ; test for the security transmitter
	srp cp jr cp jr jp	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed
TC_SEC:	srp cp jr cp jr jp	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d ReadMemory	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so
TC_SEC:	srp cp jr cp jr ip Id	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d ReadMemory	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed
TC_SEC:	srp cp jr cp jr ip Id call cp	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d ReadMemory mtemph,Radio1P5H	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed ; read the word at this address ; test for the match
TC_SEC:	srp cp jr cp jr ip Id call cp jr	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address
TC_SEC:	srp cp jr cp jr ld call cp jr cp	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match
TC_SEC:	srp cp jr cp jr ld call cp jr cp	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address
TC_SEC: NEXT_D:	srp cp jr cp jr ld call cp jr cp jr dec	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match
TC_SEC:	srp cp jr ip Id call cp jr cp jr dec eckCounte	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er:	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; test for the address ; reset the address
TC_SEC: NEXT_D:	srp cp jr ip ld call cp jr cp jr dec eckCounte	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er: TESTCOUNTER	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so  ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; test the address ; reset the address ; test the counter for in range
TC_SEC: NEXT_D:	srp cp jr ip Id call cp jr cp jr dec eckCounte	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er: TESTCOUNTER CMP,#0AAH	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so  ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; test for the address ; reset the address ; test the counter for in range ; test for within range
TC_SEC: NEXT_D: MatchedChe	srp cp jr cp jr cp jr dec eckCounte cp jr	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er: TESTCOUNTER	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so  ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; test the address ; reset the address ; test the counter for in range
TC_SEC: NEXT_D:	srp cp jr cp jr cp jr dec eckCounte cp jr	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er: TESTCOUNTER CMP,#0AAH	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so  ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; test for the address ; reset the address ; test the counter for in range ; test for within range
TC_SEC: NEXT_D: MatchedChe	srp cp jr cp jr cp jr dec eckCounte cp jr	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er: TESTCOUNTER CMP,#0AAH nz,SkipStoreCounter	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so  ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; test for the address ; reset the address ; test the counter for in range ; test for within range
TC_SEC:  NEXT_D:  MatchedChe	srp cp jr ip ld call cp jr dec eckCounte call cp jr core: call	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er: TESTCOUNTER CMP,#0AAH	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; reset the address ; test the counter for in range ; test for within range ; if not kip storing the counter
TC_SEC: NEXT_D: MatchedChe	srp cp jr ip ld call cp jr dec eckCounte call cp jr ore: call ounter:	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er: TESTCOUNTER CMP,#0AAH nz,SkipStoreCounter  STORE_D_COUNTER	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; reset the address ; test the counter for in range ; test for within range ; if not kip storing the counter
TC_SEC:  NEXT_D:  MatchedChe  TC_SEC_St  SkipStoreCo	srp cp jr ip ld call cp jr dec eckCounte call cp jr ore: call ounter: inc	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er: TESTCOUNTER CMP,#0AAH nz,SkipStoreCounter	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; reset the address ; test the counter for in range ; test for within range ; if not kip storing the counter
TC_SEC:  NEXT_D:  MatchedChe	srp cp jr ip ld call cp jr dec call cp jr core: call core: cTCH:	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er: TESTCOUNTER CMP,#0AAH nz,SkipStoreCounter  STORE_D_COUNTER address	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so  ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; reset the address ; test the counter for in range ; test for within range ; if not kip storing the counter ; save the new counter
TC_SEC:  NEXT_D:  MatchedChe  TC_SEC_St  SkipStoreCo	srp cp jr ip ld call cp jr dec eckCounte call cp jr ore: call ounter: inc	ID_B,#1d z,TC_SEC ID_B,#2d z,TC_SEC ClearRadio address,#01d  ReadMemory mtemph,Radio1P5H nz,NO_TC_MATCH mtempl,Radio1P5L nz,NO_TC_MATCH address er: TESTCOUNTER CMP,#0AAH nz,SkipStoreCounter  STORE_D_COUNTER	; test for the touch code ; jump if so ; test for the security transmitter ; jump if so ; set the start addresss for the fixed ; read the word at this address ; test for the match ; if not matching do the next address ; test for the match ; if not matching do the next address ; reset the address ; test the counter for in range ; test for within range ; if not kip storing the counter

```
ult, NEXT D
                ir
                                                       ; if not the last address then try again
GOTNO_TC_MATCH:
                        ClearRadio
        Test the radio code counter and compares
                CMP
                00 => counter the same
                FF => counter out of range
                AA => counter in range
                7F => counter within - window no resync
                Address for test in address
TESTCOUNTER:
               push
                       RP
                                                       ; save the RP
                srp
                       #CheckGroup
                                                        set the rp
               inc
                       Address
                                                        set the address to the 2x position for
               inc
                       Address
               call
                       ReadMemory
                                                        read the value
               ld
                       pradioa,MTempH
                                                        temp storage
               ld
                       pradiob,MTempL
               inc
                       Address
                       ReadMemory
               call
                                                        read the value
               sub
                       Address,#3d
                                                       ; reset the address
               ld
                       pradioc,MTempH
                                                        temp storage
               ld
                       pradiod,MTempL
                       MirrorA, pradioa
               ф
                                                        test first for the match
                       nz,NM_COUNTER
               jr
                                                       ; if not then test count position
               ср
                       MirrorB, pradiob
                       nz,NM_COUNTER
               ir
                                                       ; if not then test count position
                       MirrorC,pradioc
               ср
                       nz,NM_COUNTER
               jr
                                                       ; if not then test count position
                       MirrorD, pradiod
               ср
                       nz,NM_COUNTER
                                                       ; if not then test count position
               ld
                       CMP,#00h
                                                       ; flag the match
CounterRet:
               pop
                       RP
               ret
NM_COUNTER:
                       pradioa,#0FFH
               ф
                                                      ; test for the roll over
               jr
                       nz,NORMALN
                                                      ; if not test normally
                       pradiob,#0FFH
               ф
                                                      ; test for the roll over
               jr
                       nz,NORMALN
                                                      ; if not test normally
               ф
                       MirrorA,#0H
                                                       ; test for the rollover
               jr
                       nz,NORMALN
                                                       ; if not test normally
                       MirrorB,#0H
               ср
                                                      ; test for the rollover
                       nz, NORMALN
                                                       ; if not test normally
                                                       at roll com past add pres
               call
                       Complement
               add
                       pradiod, Mirror D
                                                       add the 2
               adc
                      pradioc, MirrorC
               adc
                      pradiob, Mirror B
               adc
                      pradioa, Mirror A
```

```
cp
                        pradioc,#12d
                                                      ; window 3072 or 1024 activations
                        ule,COUNTOK
                jr
 COUNTOUT:
                call
                        Complement
                                                      ; find the - difference
                ср
                        pradioa.#00
                                                      test for within 00000400H
                        nz,OutOfWindow
                jr
                ср
                       pradiob,#00
                jr
                       nz,OutOfWindow
                ср
                       pradioc,#00000100B
                ir
                       ugt,OutOfWindow
                lđ
                       CMP.#7FH
                                                      ; mark the -window function
                       CounterRet
                                                      ; return
 OutOfWindow:
                ld
                       CMP,#0FFH
                                                     ; set the bad count flag
               jr
                       CounterRet
                                                     ; return
 COUNTOK:
                ld
                       CMP,#0AAH
                                                     ; set the count flag ok
               jr
                       CounterRet
                                                     ; return
 NORMALN:
               sub
                       pradiod, MirrorD
                                                     ; subtrace to find difference
               sbc
                       pradioc, MirrorC
               sbc
                       pradiob, Mirror B
               sbc
                       pradioa, Mirror A
               call
                       Complement
                                                     ; make positive
               ср
                       pradioa,#00
                                                     test for to large
               jr
                       nz,COUNTOUT
                                                     ; if so out of window
               ср
                       pradiob,#00
                                                     ; test for to large
               jr
                       nz,COUNTOUT
                                                     ; if so out of window
               ср
                       pradioc,#11D
                                                     ; window for 1024
               jr
                       ule, COUNTOK
               įr
                       COUNTOUT
Complement:
               com
                      pradiod
                                                     ; Complement the temp reg
                      pradioc
               com
               com
                      pradiob
               com
                      pradioa
               ret
       TESTMATCH TEST THE NON ROLLING PART OF ANY CODE IF THERE
       IS A MATCH RETURNS THE ADDRESS ELSE RETURNS FF
TESTMATCH:
TEST_D_CODES:
                      address
                                                    ; start at address 0
NEXT_D_CODE:
              call
                      ReadMemory
                                                    ; read the word at this address
              ср
                      mtemph,RadioP5H
                                                    ; test for the match
              jr
                      nz,NO_D_MATCH
                                                    ; if not matching then do next address
```

; test for the match

; if not matching then do next address

mtempl,RadioP5L

nz,NO\_D\_MATCH

ф

```
inc
                        address
                                                      ; set the second half of the code
                        ReadMemory
                call
                                                      ; read the word at this address
                ср
                        mtemph,Radio1P5H
                                                      ; test for the match
                įr
                        nz,NO_D_MATCH2
                                                      ; if not matching do the next address
                ср
                        mtempl,Radio1P5L
                                                      ; test for the match
                jr
                        nz,NO_D_MATCH2
                                                      ; if not matching do the next address
                dec
                        address
                                                      ; reset the address
                        TMEXIT
                                                      ; return with the address of the match
 NO_D_MATCH:
                       address
                                                      ; set the address to the next code
 NO_D_MATCH2:
                add
                       address,#3d
                                                      ; set the address to the next code
                       address,#1CH
                                                      ; test for the last address
                jr
                       ult,NEXT_D_CODE
                                                      ; if not the last address then try again
GOTNO_D_MATCH:
                ld
                       address,#0FFH
                                                      ; set the no match flag
                ret
TMEXIT:
                ld
                       LastM1Match, LastMatch
                                                      ; delay line
               ld
                       LastMatch,address
                                                      ; save the address for radio timeout
               ret
; LEARN DEBOUNCES THE LEARN SWITCH 80mS
; TIMES OUT THE LEARN MODE 30 SECONDS
; DEBOUNCES THE LEARN SWITCH FOR ERASE 6 SECONDS
LEARN:
                       #LearnModeGroup
               srp
                                                     ; set the group
               ср
                       cmdswitch,#236D
                                                      ; test for the debouncer release
               jr
                       nz,ReleaseDone
                                                      ; if not then test for set
               cir
                       cmdswitch
                                                     ; clear the debouncer
ReleaseDone:
                       cmdswitch,#20D
               ф
                                                     ; test for switch 2 set
                       UGT, CLEARRA
               jr
multi2:
               ф
                       cmdswitch,#20D
                                                     ; test for switch 2 set
               jr
                       nz,TESTLEARN
                                                     ; if not then test learn
SW2isSET:
               ld
                      cmdswitch,#0FFH
                                                     ; set the debouncer
CMDSW:
               clr
                      mono
                                                     ; clear the timer
               xor
                      P2,#01000000B
                                                     ; toggle
               Or
                      P2,#00011000B
                                                     ; set
CLEARRA:
               clr
                      rto
TESTLEARN:
                      learndb,#236D
               Ср
                                                     ; test for the debounced release
              jr
                      nz, LEARNNOTRELEASED
                                                     ; if not released then jump
```

	clr	learndb	; clear the debouncer
	ret		; return
LEARNNOTRE	LEASE	D:	
	cp jr	learnt,#0FFH nz,INLEARN learndb,#20D	; test for learn mode ; if in learn jump
SETLEARN:	cp jr	nz,ERASETEST	; test for debounce period ; if not then test the erase period
	cir	learnt	; clear the learn timer
	ld	leamdb,#0FFH	; set the debouncer
ERASETEST:	and	P2,#11111101b	; turn on the led
	ср	leamdb,#0FFH	; test for learn button active
	jr	nz,ERASERELEASE	; if button released set the erase timer
	ф	eraset,#0FFH	; test for timer active
	jr	nz,ERASETIMING	; if the timer active jump
	cir	eraset	clear the erase timer
ERASETIMING	à:		,
	ср	eraset,#48D	; test for the erase period
	jr	z,ERASETIME	; if timed out the erase
ERASETIME:	ret		; else we return
	or	P2,#0000010b	; turn off the led
	ld	skipradio,#0FFH	; set the flag to skip the radio read
	call	CLEARCODES	; clear all codes in memory
	clr	skipradio	; reset the flag to skip radio
		- F ·	, reset the hag to skip radio
	ld	learnt,#0FFH	; set the learn timer
	ret		; return
ERASERELEAS	SE:		• 11
	ld ret	eraset,#0FFH	; turn off the erase timer ; return
INLEARN:			
	ф	learndb,#20D	; test for the debounce period
	jr	nz,TESTLEARNTIMER	; if not then test the learn timer
	İd	leamdb,#0FFH	; set the learn db
TESTLEARNTI	MER:	·	, 551 815 154111 45
	ф	learnt,#240D	; test for the learn 30 second timeout
	jr	nz,ERASETEST	; if not then test erase
learnoff:			,
	or	P2,#0000010B	: turn off the led
	ld	learnt,#0FFH	; set the learn timer
	ld	leamdb,#0FFH	; set the learn debounce
	jr	ERASETEST	; test the erase timer

; WRITE WORD TO MEMORY ; ADDRESS IS SET IN REG ADDRESS ; DATA IS IN REG MTEMPH AND MTEMPL ; RETURN ADDRESS IS UNCHANGED WRITEMEMORY: RP ; SAVE THE RP #LearnEeGroup ; set the register pointer srp call **STARTB** ; output the start bit ld serial,#00110000B ; set byte to enable write call SERIALOUT ; output the byte and csport,#csl ; reset the chip select call **STARTB** ; output the start bit serial,#01000000B ld ; set the byte for write serial, address ; or in the address or **SERIALOUT** ; output the byte call ld serial, mtemph ; set the first byte to write SERIALOUT call ; output the byte ld serial, mtempl ; set the second byte to write **SERIALOUT** call ; output the byte call **ENDWRITE** ; wait for the ready status call STARTB ; output the start bit ld serial,#00000000B ; set byte to disable write call SERIALOUT ; output the byte and csport,#csl ; reset the chip select : reset the RP pop ret

READ WORD FROM MEMORY
ADDRESS IS SET IN REG ADDRESS
DATA IS RETURNED IN REG MTEMPH AND MTEMPL
ADDRESS IS UNCHANGED

\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*\*

### ReadMemory:

RP push srp #LearnEeGroup ; set the register pointer **STARTB** call ; output the start bit serial,#10000000B ; preamble for read ld serial, address or ; or in the address call **SERIALOUT** ; output the byte **SERIALIN** call ; read the first byte ld mtemph, serial ; save the value in mtemph call SERIALIN ; read teh second byte ld mtempl,serial ; save the value in mtempl and csport,#csl ; reset the chip select RP pop ret

## WRITE D CODE TO 4 MEMORY ADDRESS

CODE IS IN Radio1P5H Radio1P5L RadioP5H RadioP5L CODE IS IN Count1P5H Count1P5L CountP5H CountP5L

WRITE\_D\_CODE:

push RP

srp #LearnEeGroup Id mtemph,RadioP5H ; set the register pointer; transfer the data

```
ld
                         mtempl,RadioP5L
                 call
                         WRITEMEMORY
                                                        ; write the temp bits
                 inc
                         address
                                                        ; next address
                 ld
                         mtemph, Radio 1P5H
                                                        ; transfer the data
                 ld
                         mtempl, Radio 1P5L
                 call
                         WRITEMEMORY
                                                        ; write the temps
                 inc
                         address
                                                        ; next address
 STORE_COUNTER:
                         mtemph, Mirror A
                                                        ; transfer the data
                 ld
                        mtempl, MirrorB
                        WRITEMEMORY
                 call
                                                        ; write the temps
                 inc
                        address
                                                        ; next address
                 ld
                        mtemph, Mirror C
                                                        ; transfer the data
                 ld
                        mtempl, MirrorD
                 call
                        WRITEMEMORY
                                                        ; write the temps
                 dec
                        address
                                                        reset the address
                 dec
                        address
                 dec
                        address
                 pop
                        RP
                ret
                                                       ; return
 STORE_D_COUNTER:
                push
                        RP
                srp
                        #LearnEeGroup
                                                       ; set the register pointer
                inc
                        address
                inc
                        address
                        STORE COUNTER
                jr
 ; START BIT FOR SERIAL NONVOL
 ; ALSO SETS DATA DIRECTION AND AND CS
STARTB:
                ld
                        P2M, #P2M INIT
                                                       ; set port 2 mode
                and
                        csport,#csl
                and
                        clkport,#clockl
                                                       ; start by clearing the bits
                and
                        dioport,#dol
                or
                        csport,#csh
                                                       ; set the chip select
                or
                        dioport,#doh
                                                       ; set the data out high
                or
                        clkport,#clockh
                                                       ; set the clock
                and
                        clkport,#clockl
                                                      ; reset the clock low
                and
                        dioport,#dol
                                                      ; set the data low
                                                       ; return
 END OF CODE WRITE
ENDWRITE:
               ld
                       P2M,#(P2M_INIT+1)
                                                      ; set port 2 mode
               and
                       csport,#csi
                                                      ; reset the chip select
               nop
                                                      ; delay
               or
                       csport,#csh
                                                      ; set the chip select
               WDT
                                                      ; kick the dog
ENDWRITELOOP:
               ld
                       temph,dioport
                                                      ; read the port
```

```
and
                         temph,#doh
                                                         ; mask
                         z, ENDWRITELOOP
                 jr
                                                         ; if the bit is low then loop
                 and
                         csport,#csl
                                                         ; reset the chip select
                 ld
                         P2M, #P2M INIT
                                                         ; set port 2 mode forcing output mode
                 ret
  SERIAL OUT
  OUTPUT THE BYTE IN SERIAL
 SERIALOUT:
                         P2M,#P2M_INIT
                 ld
                                                         ; set port 2 mode
                 ld
                         templ,#8H
                                                         ; set the count for eight bits
SERIALOUTLOOP:
                 rlc
                         serial
                                                         ; get the bit to output into the carry
                 jr
                         nc,ZEROOUT
                                                         ; output a zero if no carry
ONEOUT:
                 or
                         dioport,#doh
                                                         ; set the data out high
                        clkport,#clockh
                 or
                                                         ; set the clock high
                 and
                        clkport,#clockl
                                                         ; reset the clock low
                 and
                        dioport,#dol
                                                         ; reset the data out low
                dinz
                        templ, SERIALOUTLOOP
                                                         ; loop till done
                ret
                                                         ; return
ZEROOUT:
                and
                        dioport,#dol
                                                         ; reset the data out low
                or
                        clkport,#clockh
                                                         ; set the clock high
                and
                        clkport,#clockl
                                                         ; reset the clock low
                and
                        dioport,#dol
                                                         ; reset the data out low
                djnz
                        templ, SERIALOUTLOOP
                                                         ; loop till done
                                                         ; return
 SERIAL IN
; INPUTS A BYTE TO SERIAL
SERIALIN:
                ld
                        P2M,\#(P2M_INIT+1)
                                                        ; set port 2 mode
                Id
                        templ,#8H
                                                        ; set the count for eight bits
SERIALINLOOP:
                        clkport,#clockh
                or
                                                        ; set the clock high
                rcf
                                                        ; reset the carry flag
                ld
                        temph,dioport
                                                        ; read the port
                and
                        temph,#doh
                                                        ; mask out the bits
                įr
                        z,DONTSET
                scf
                                                        ; set the carry flag
DONTSET:
                ric
                        serial
                                                        ; get the bit into the byte
                and
                        cikport,#clockl
                                                        ; reset the clock low
                dinz
                        templ, SERIALINLOOP
                                                        ; loop till done
                ret
                                                        ; return
```

.**************************************			
; CLEAR PAGE 0 CODES IN THE MEMORY			
;*************************************			*****
	push	RP	
	di		; disable interrupts
	ld	SkipRadio,#0FFH	;
	srp	#LearnEeGroup	; set the register pointer
	ld	Radio1P5H,#0FFH	; set the codes to illegal codes
	ld ld	Radio1P5L,#0FFH	;
	id	RadioP5H,#0FFH RadioP5L,#0FFH	•
	clr	address	; ; set the page
	ld	cmp,#07d	; erase 7 values
ClearLoop:		• //	, or also / raides
	call	WRITE_D_CODE	; clear this address
	add	address,#4d	; next clear address
	djnz	cmp,ClearLoop	;
	cir	mtemph	; clear data
	cir	mtempl	
	ld call	address,#1FH	; set the address
	pop	WRITEMEMORY RP	; ;
	ret	1 11	, ; return
			, return
, TILATED LIDE	******	*******	**************************************
; IIMER UPD/	AIE FRO	DM INTERUPT EVERY .256mS	<b>3</b>
; TimerOneInt:			*****
interonent.	inc	TaskSwitch	t not to the next quitab
	ld	IMR,#RETURN_IMR	; set to the next switch ; turn on the interrupt
	tm	TaskSwitch,#0000001b	; even odd
	jr	nz,SkipRsRoutine	; do rs232 .5 mS
	call	R\$232	; do the serial
SkipRsRoutine	<b>:</b> :		,
	tm	TaskSwitch,#00000011B	; test for task 0,1,2 or 3
TACKO.	jr	z,TASK1	; task 1 every 1 mS
TASK0:	izat		
	iret		
TASK1:			•
ONEMS:	push	RP	
	push	RP	
	push srp	#LearnModeGroup	; set the register pointer
	srp inc	#LearnModeGroup T4MS	; set the register pointer ; increment the 4mS timer
	srp inc inc	#LearnModeGroup T4MS T125MS	; increment the 4mS timer ; increment the 125 mS timer
	srp inc inc cp	#LearnModeGroup T4MS T125MS T4MS,#4D	; increment the 4mS timer ; increment the 125 mS timer ; test for the time out
FOLIBMS:	srp inc inc	#LearnModeGroup T4MS T125MS	; increment the 4mS timer ; increment the 125 mS timer
FOURMS:	srp inc inc cp jp	#LearnModeGroup T4MS T125MS T4MS,#4D nz,TEST125	; increment the 4mS timer ; increment the 125 mS timer ; test for the time out ; if not true then jump
FOURMS:	srp inc inc cp jp	#LearnModeGroup T4MS T125MS T4MS,#4D nz,TEST125	; increment the 4mS timer ; increment the 125 mS timer ; test for the time out ; if not true then jump ; reset the timer
FOURMS:	srp inc inc cp jp clr cp	#LearnModeGroup T4MS T125MS T4MS,#4D nz,TEST125 T4MS rto,#0FFh	; increment the 4mS timer ; increment the 125 mS timer ; test for the time out ; if not true then jump ; reset the timer ; test for the end of the rto
FOURMS:	srp inc inc cp jp	#LearnModeGroup T4MS T125MS T4MS,#4D nz,TEST125	; increment the 4mS timer ; increment the 125 mS timer ; test for the time out ; if not true then jump ; reset the timer ; test for the end of the rto ; if the radio timeout ok then skip
FOURMS: - RTOOK:	srp inc inc cp jp clr cp jr	#LearnModeGroup T4MS T125MS T4MS,#4D nz,TEST125 T4MS rto,#0FFh z,RTOOK	; increment the 4mS timer ; increment the 125 mS timer ; test for the time out ; if not true then jump ; reset the timer ; test for the end of the rto

MONOOK:	inc jr dec	mono nz,MONOOK mono	; increment the mono time out ; if the mono timeout ok then skip ; back turn ;
	cp jr	SwitchSkip,#00 nz,TEST125	; test for the skip switches command ;
TESTSW1:			
	ţm	P2,#00100000B	; test switch one
	jr ∽	z,SW1SET	; if set jump
	ср ir	LearnDebounce,#00H z,TESTSW2	; test for min number
	dec	LearnDebounce	; if at min skip dec ; dec debouncer down
CM4 CET.	jr	TESTSW2	; next
SW1SET:	œ	LoarnDobourse #0FF1	
	ор jr	LearnDebounce,#0FFH z,TESTSW2	; test for the max number
	inc	LearnDebounce	; if at max skip inc ; inc the debouncer
TECTOMO			, 4.10 4000411001
TESTSW2:	tm	P2 #00000100P	
	jr	P2,#00000100B z,SW2SET	; test switch two
	çp Ç	CmdSwitch,#00H	; if set jump ; test for min number
	jr	z,TESTSWDB	; if at min skip dec
	dec	CmdSwitch	; dec debouncer down
SW2SET:	jr	TESTSWDB	; next
	ср	CmdSwitch,#0FFH	; test for the max number
	jr	z,TESTSWDB	; if at max skip inc
	inc	CmdSwitch	; inc the debouncer
TESTSWDB:			
TEST125:			
1251125	ф	T125MS,#125D	And for the N
	ir	z,ONE25MS	; test for the time out ; if true the jump
	рор	RP	, it tide the jump
ONE25MS:	iret		
TOG:			
	ei		; enable the interrupts
	clr	T125MS	; reset the timer
	сф	SysDisable,#0FFH	; test for the top
	jr inc	z,DO12 SysDisable	and the second setting of the second setting of the second setting of the second secon
DO12:		C) SDISADIE	; count off the system disable timer
	ср	learnt,#0FFH	; test for overflow
	jr in a	z,LEARNTOK	; at roll over skip
LEARNTOK:	inc	learnt	; increase the learn timer
	ф	eraset,#0FFH	; test for overflow
	jr	z,ERASET1OK	; if at roll skip
ERASET1OK:	inc	eraset	; increase the erase timer
ENASETION:	рор	RP	
	F -F		

### **RS232 DATA ROUTINES**

enter rs232 start with word to output in rs232do

### RS232OSTART:

pusn	rp .	; save the rp
srp	#TimerGroup	; set the group pointer
clr	RSStart	; one shot
ld	rs232odelay,#6d	; set the time delay to 3. mS
clr	rs232docount	; start with the counter at 0
and	RS232OP,#RS232OC	; clear the output
jr	NORSOUT	•

RS232:

ср	RSStart,#0FFH	; test for the start flag
ir	z BS232OSTART	_

RS232OUTPUT

J1:		
push srp	rp #TimerGroup	; save the rp ; set the group pointer
cp ir	rs232docount,#11d nz.RS232R	; test for last
or JR	RS232OP,#RS232OS NORSOUT	; set the output idle

RS232R:

djnz inc scf	rs232odelay,NORSOUT rs232docount	; cycle count time delay ; set the count for the next cycle ; set the carry flag for stop bits
rrc	rs232do	; get the data into the carry
jr	c,RS232SET	; if the bit is high then set
and	RS232OP,#RS232OC	; clear the output
ir	SETTIME	; find the delay time

RS232SET:

- 1 -	or	RS232OP,#RS232OS	; set the output
::			

SETTIME:

ld	rs232odelay,#6d	; set the data output delay
tm	rs232docount,#00000001b	; test for odd words
jr	z,NORSOUT	; if even done
ld	rs232odelay,#7d	; set the delay to 7 for odd
	-	this gives 6.5 * 512mS

NORSOUT: RS232INPLIT

RS232INPUT:			
	СР	rs232dicount,#0FFH	; test mode
	jr	nz,RECEIVING	; if receiving then jump
	tm	RS232IP,#RS232IM	; test the incoming data
-	jr	nz,NORSIN	; if the line is still idle then skip
	clr	rs232dicount	; start at 0
	ld	rs232idelay,#3	; set the delay to mid
RECEIVING:			•
	djnz	rs232idelay,NORSIN	; skip till delay is up

	inc cp jr tm rcf	rs232dicount rs232dicount,#10d z,DIEVEN RS232IP,#RS232IM	; bit counter ; test for last timeout ; test the incoming data ; clear the carry		
OKIDOETTING	jr scf	z,SKIPSETTING	; if input bit not set skip setting carry ; set the carry		
SKIPSETTING:					
•	rrc Id tm jr Id jr	rs232di rs232idelay,#6d rs232dicount,#00000001b z,NORSIN rs232idelay,#7 NORSIN	; save the data into the memory ; set the delay ; test for odd ; if even skip ; set the delay		
DIEVEN:	,				
	ld ld clr	rs232dicount,#0FFH rscommand,rs232di RSCount	; turn off the input till next start ; save the value ; clear the counter		
NORSIN:					
	pop ret	rp	; return the rp		
	Fill Fill Fill Fill Fill Fill				
.end					

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